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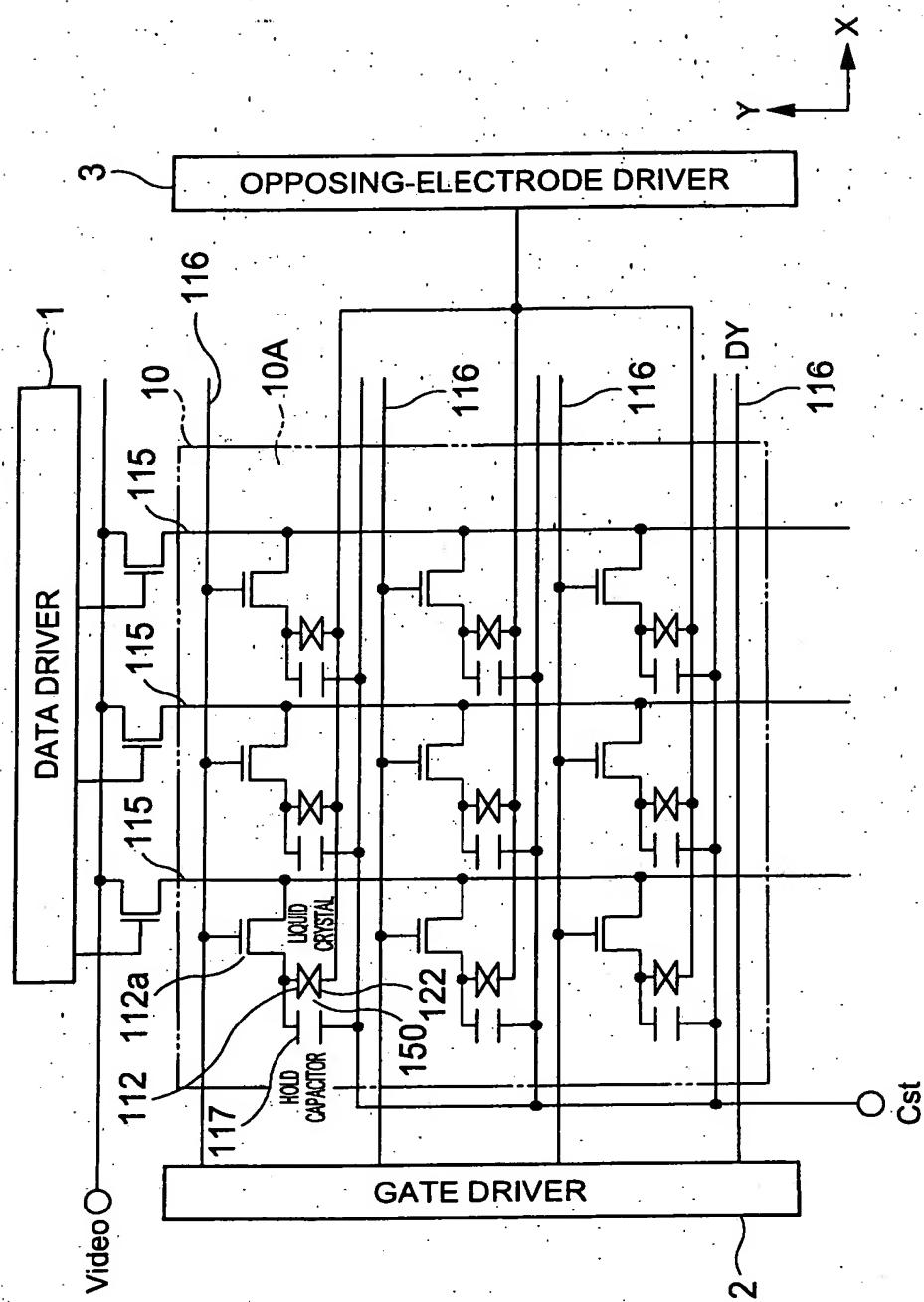
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FIG. 1



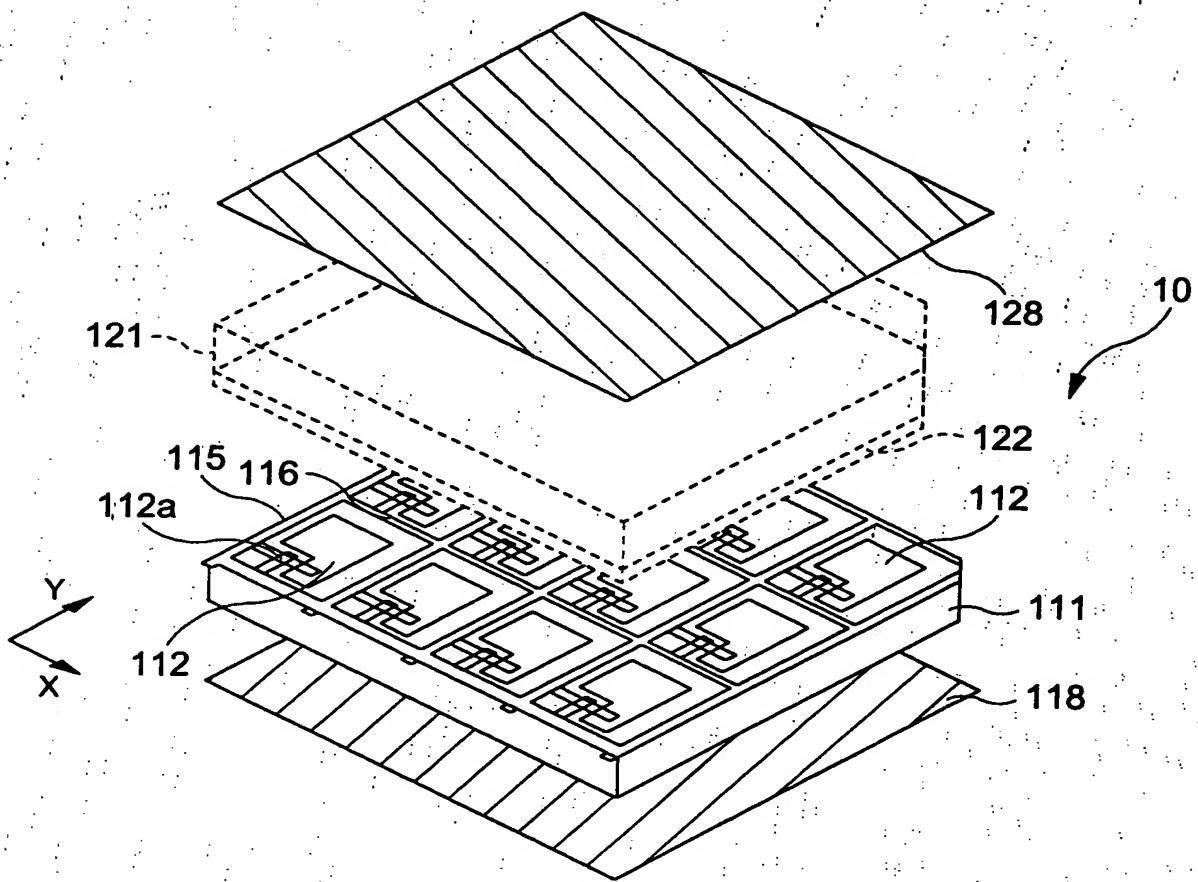


FIG. 2

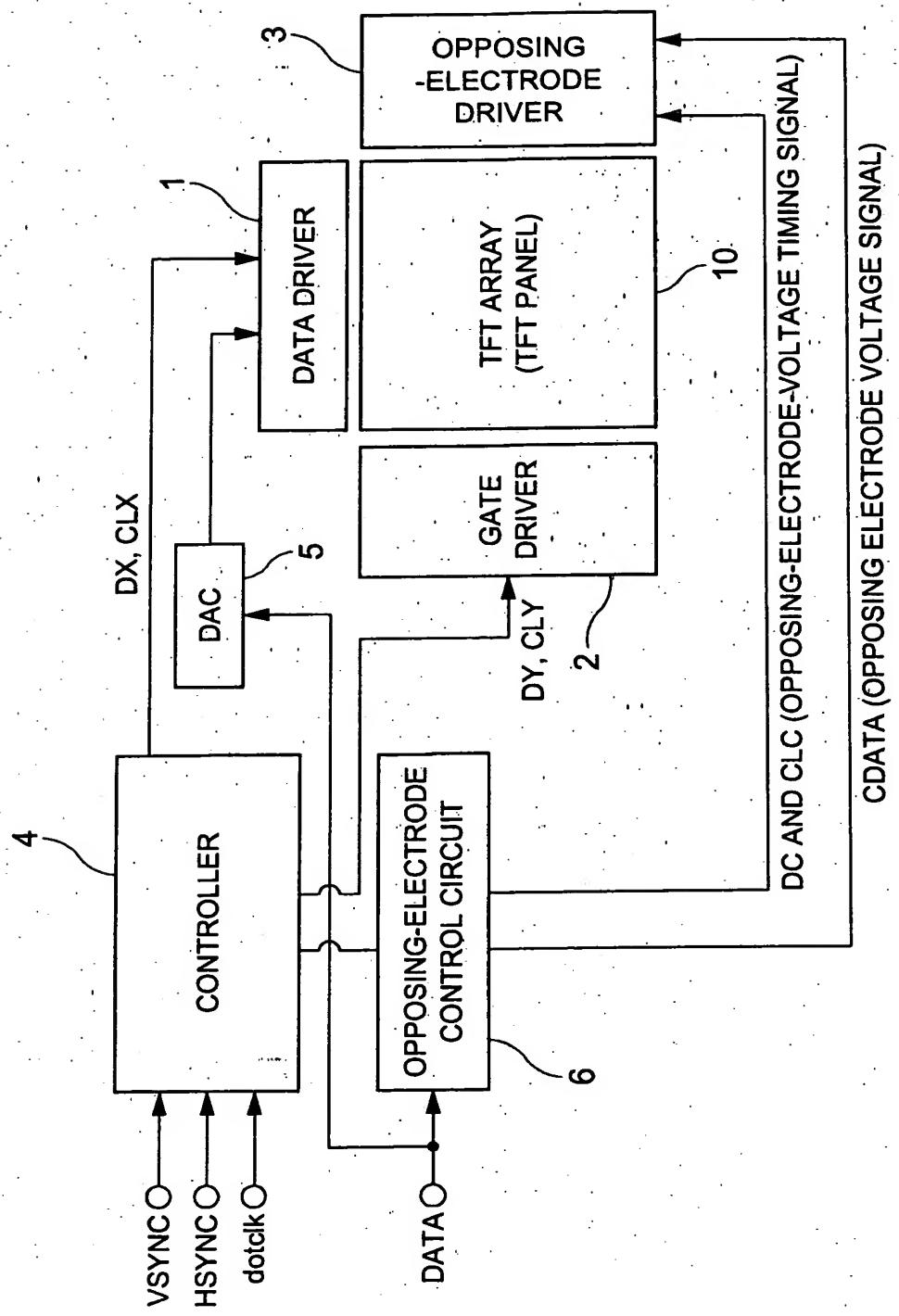
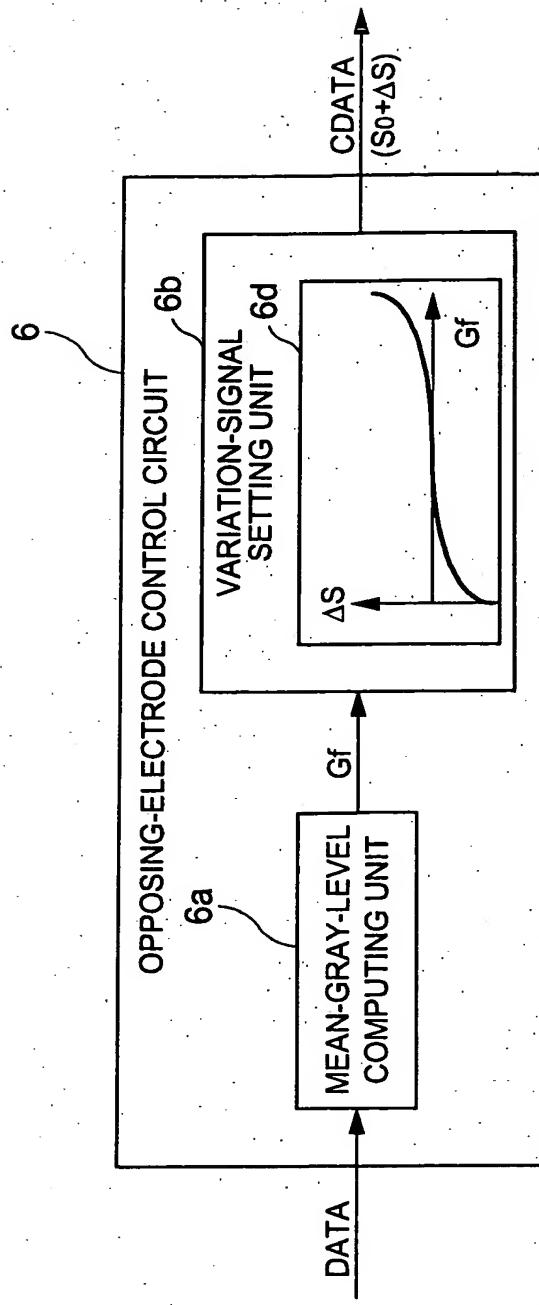


FIG. 3

CDATA (OPPOSING ELECTRODE VOLTAGE TIMING SIGNAL)

FIG. 4



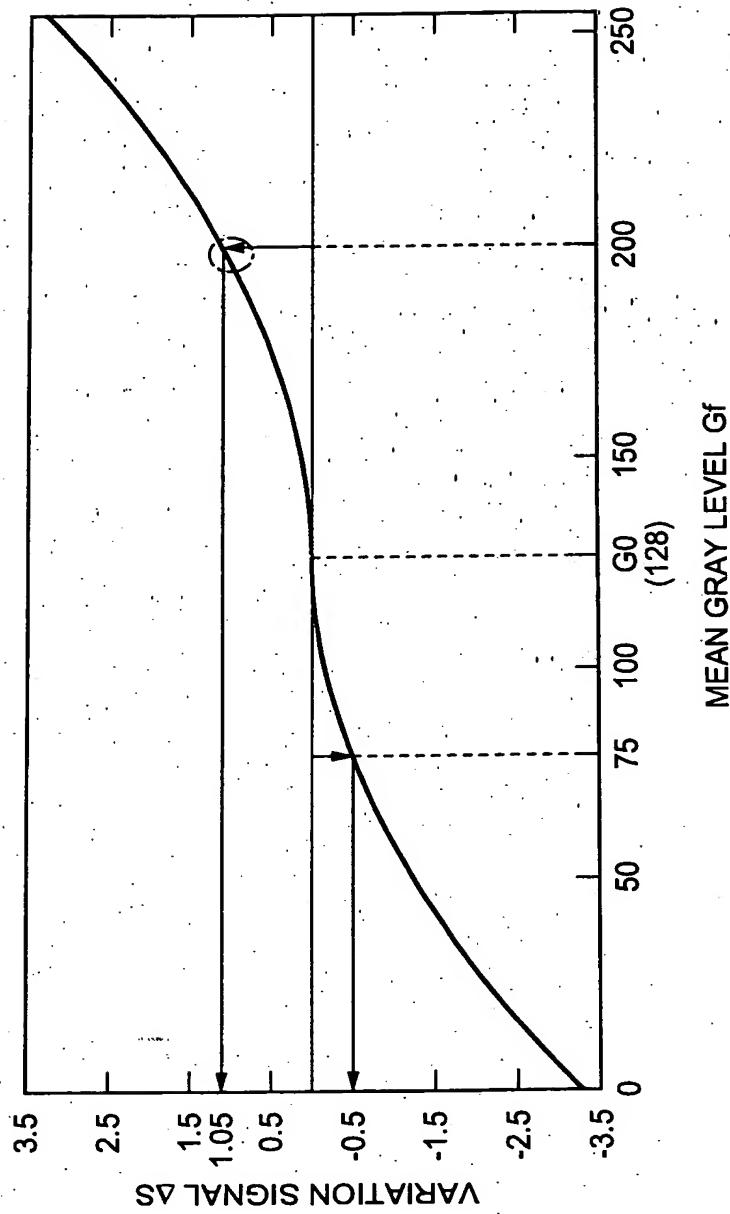


FIG. 5

FIG. 6A

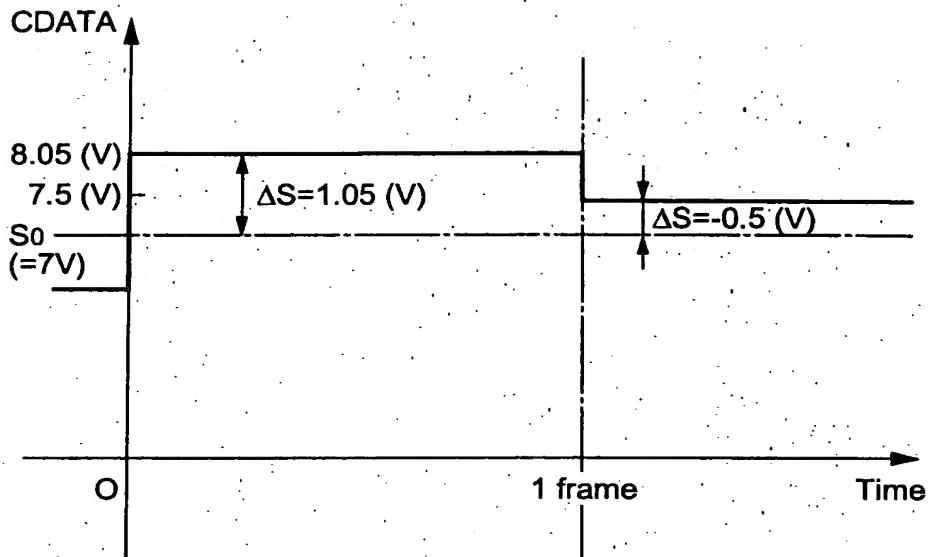
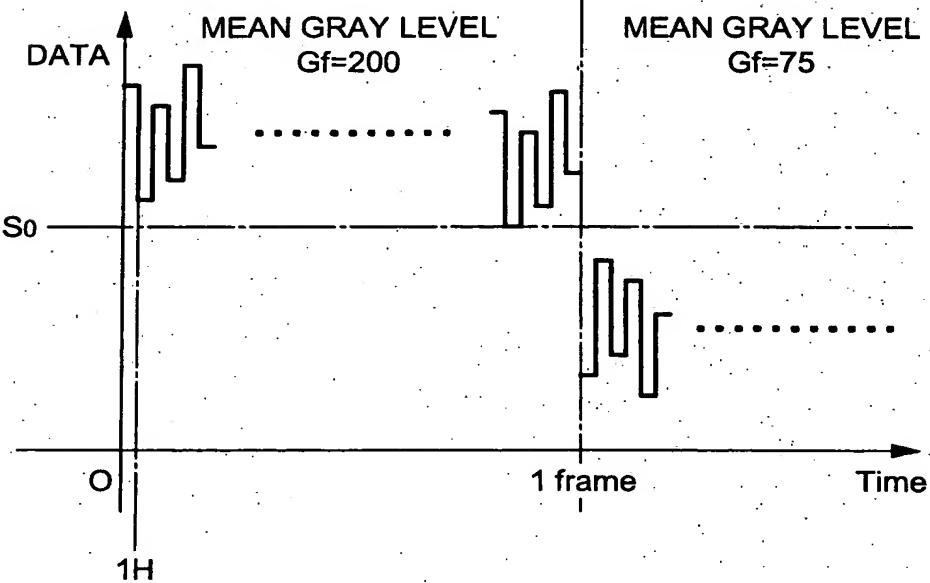


FIG. 6B



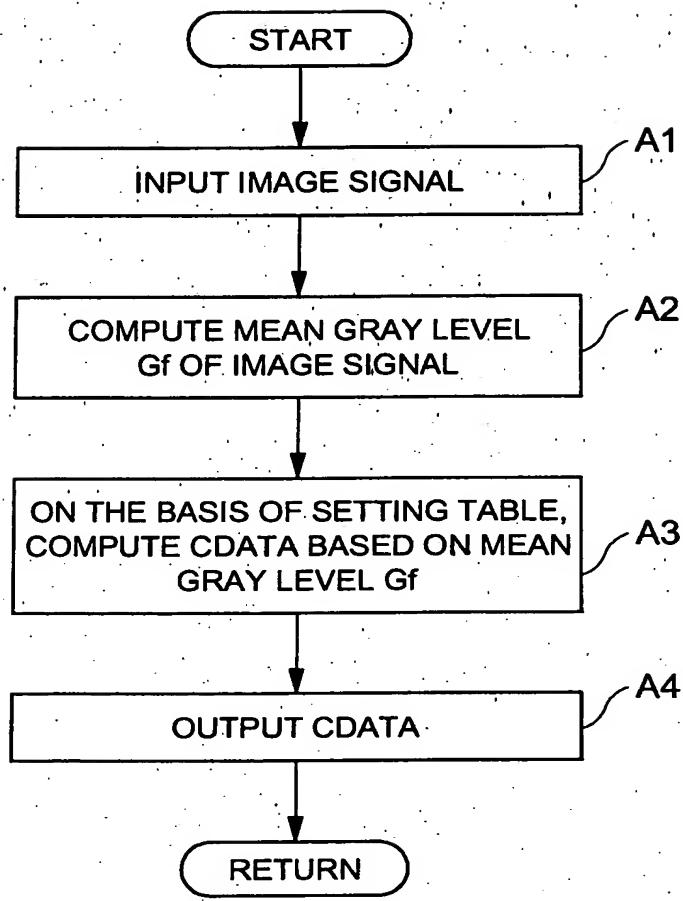


FIG. 7

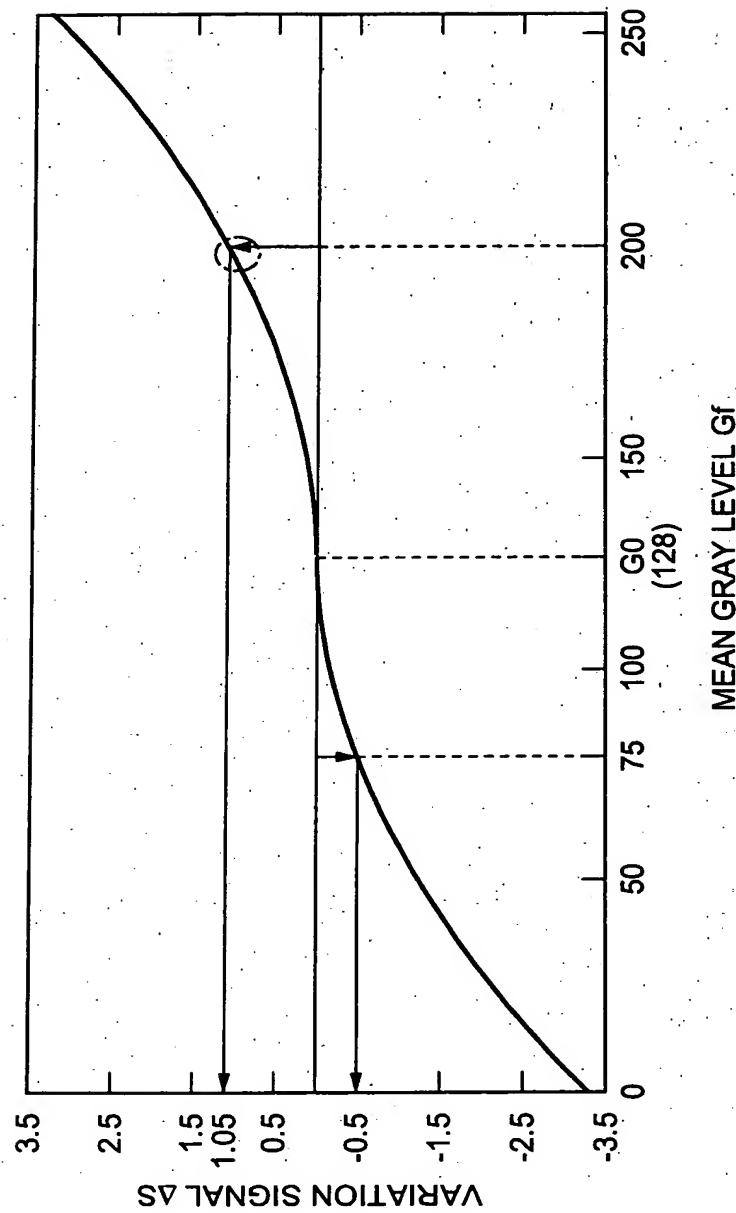


FIG. 8

FIG. 9A

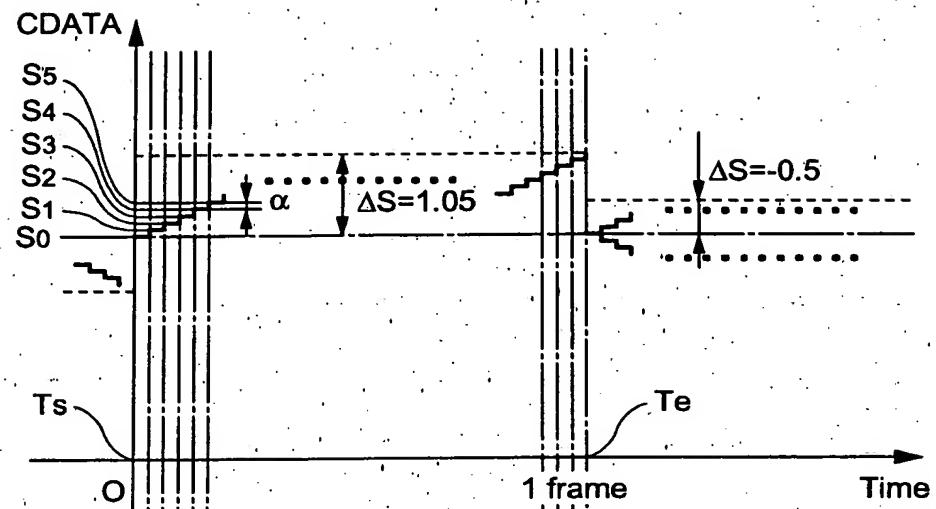
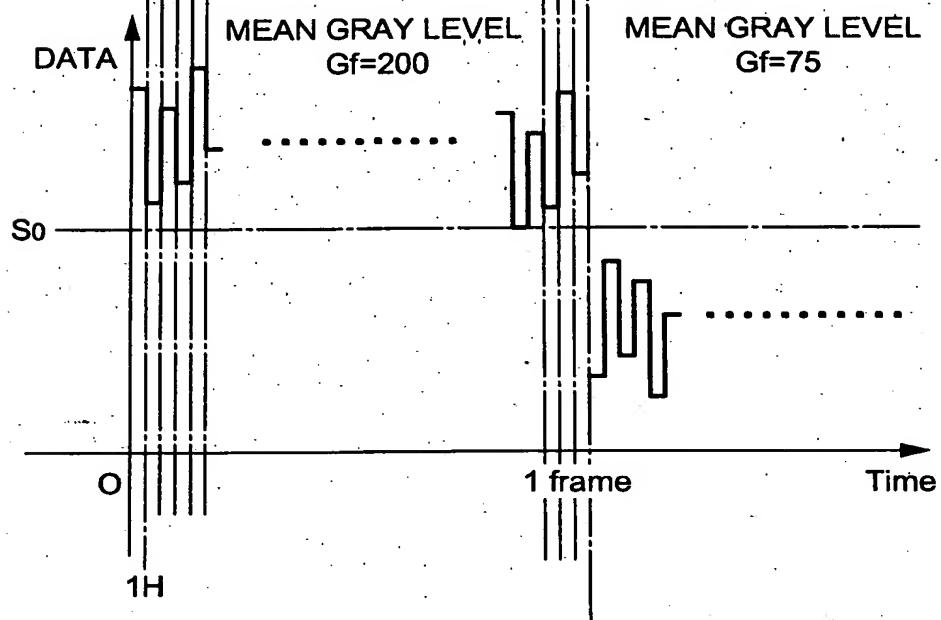


FIG. 9B



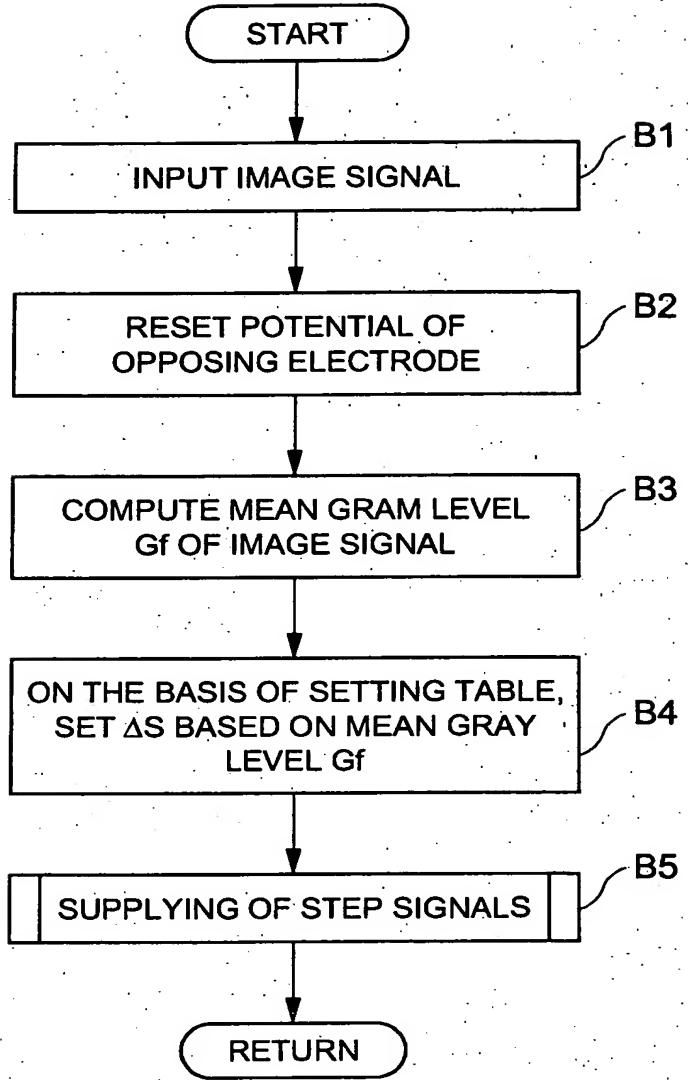


FIG. 10

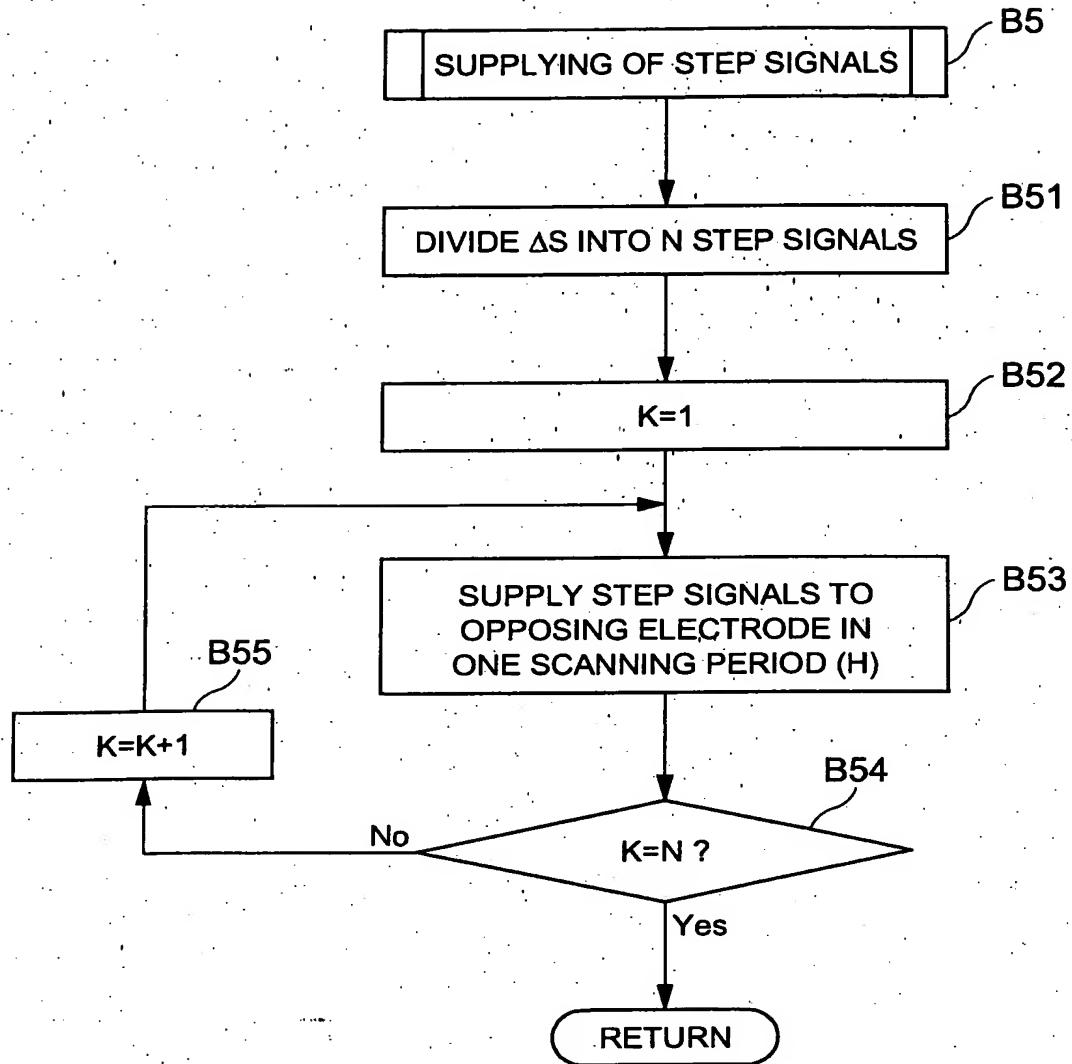
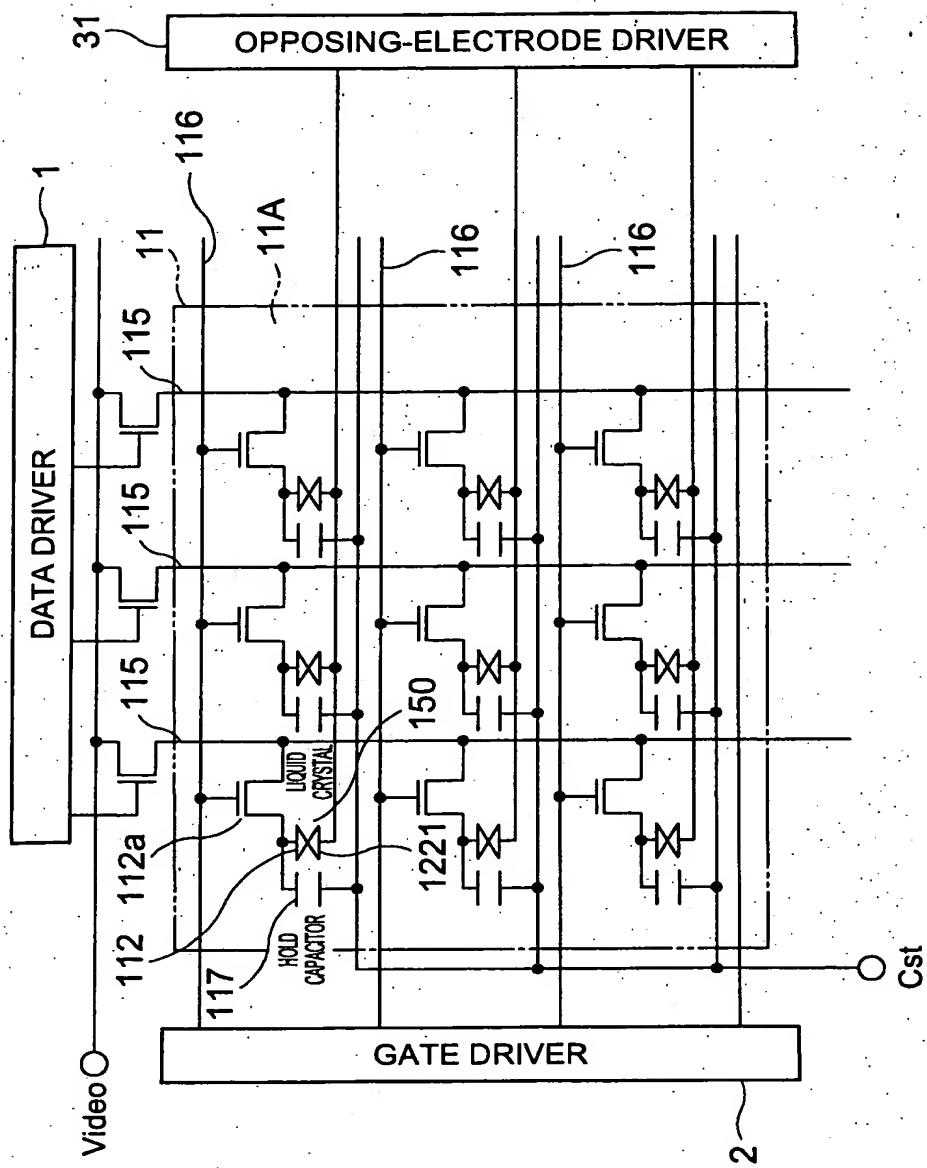


FIG. 11

FIG. 12



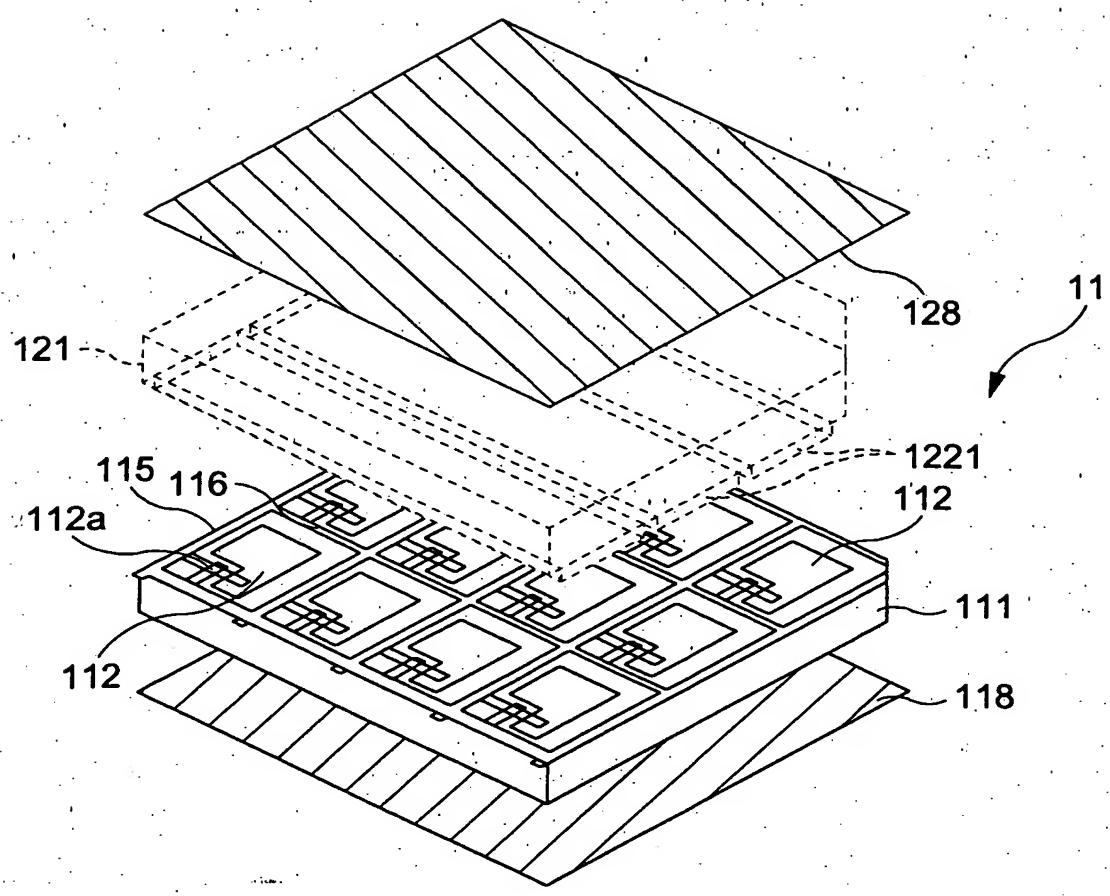


FIG. 13

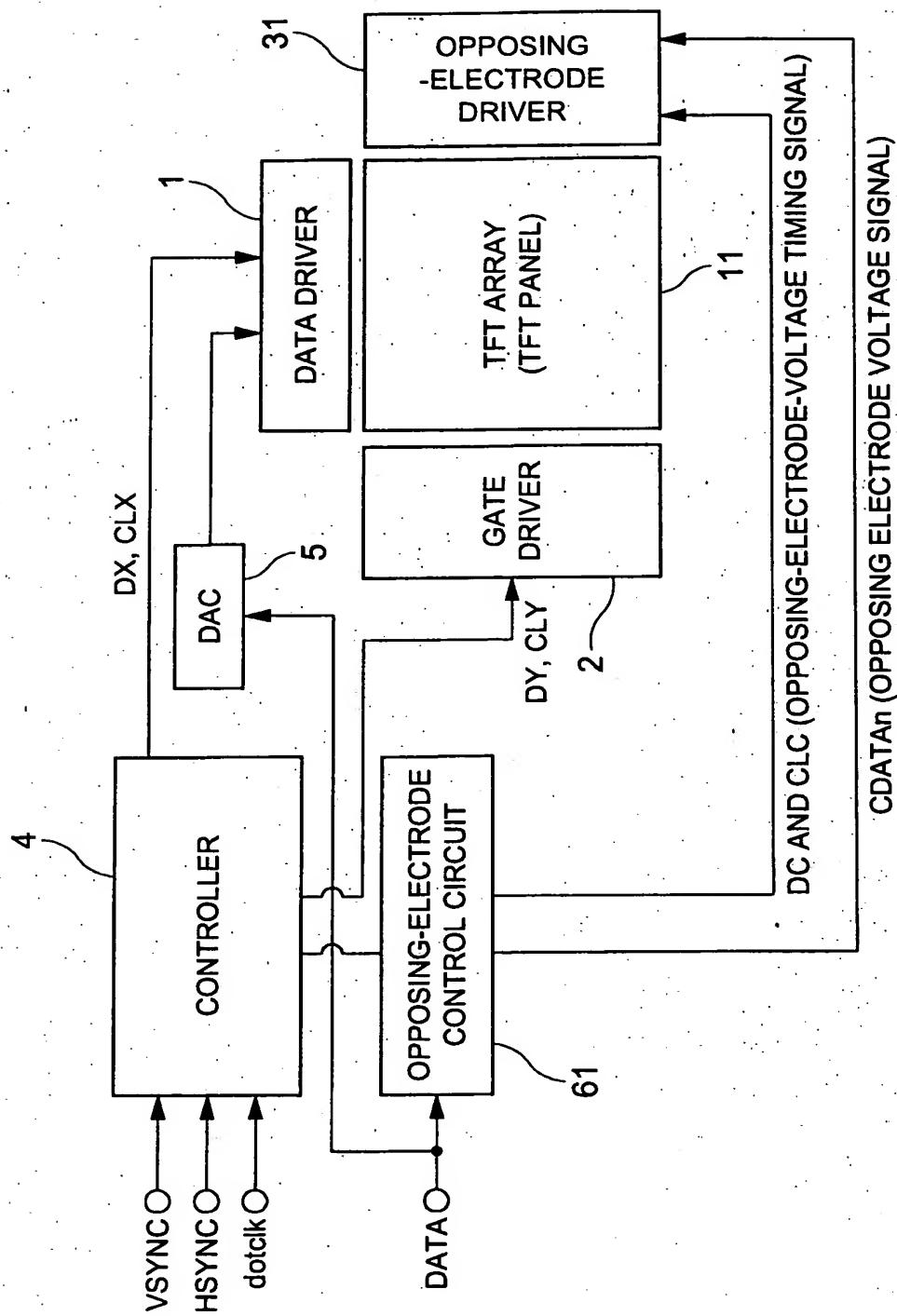
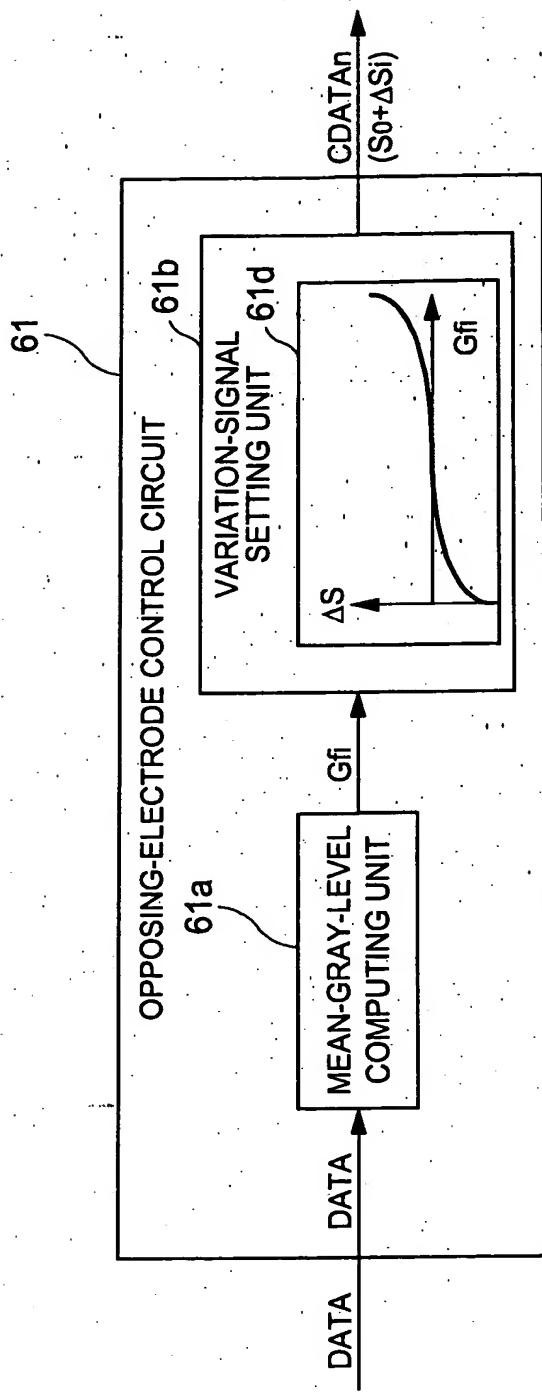


FIG. 14

FIG. 15



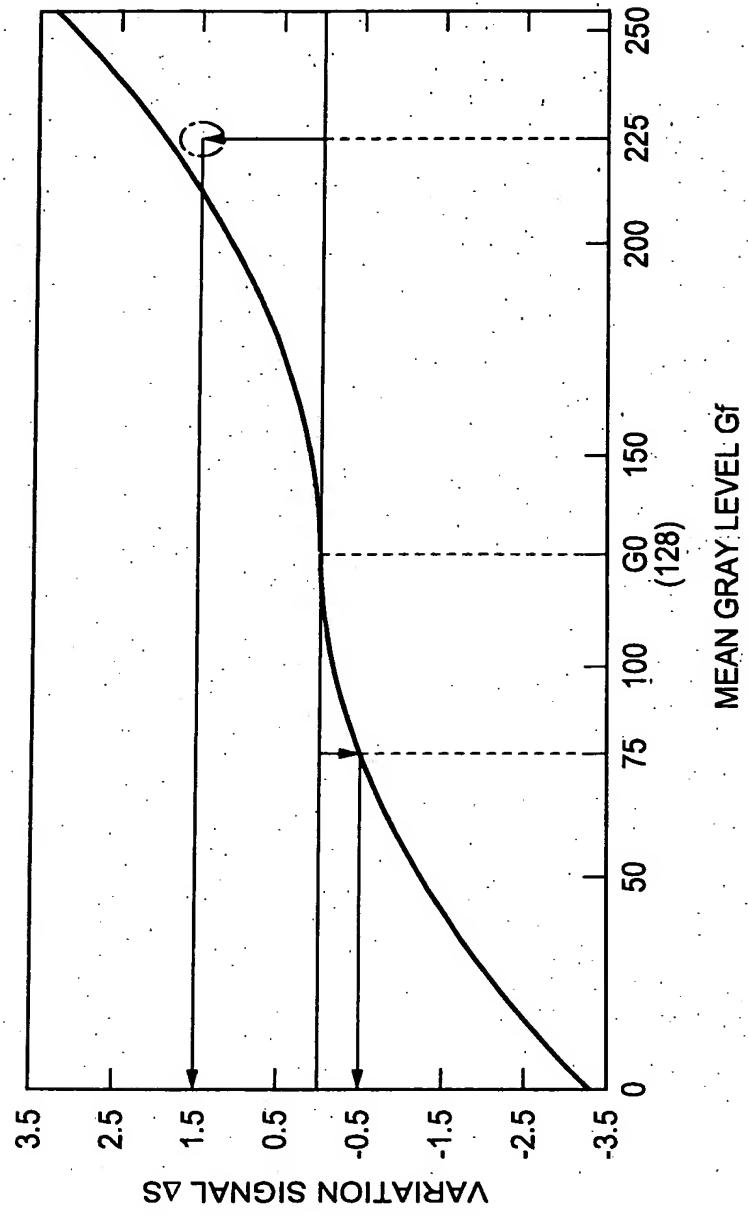


FIG. 16

FIG. 17A

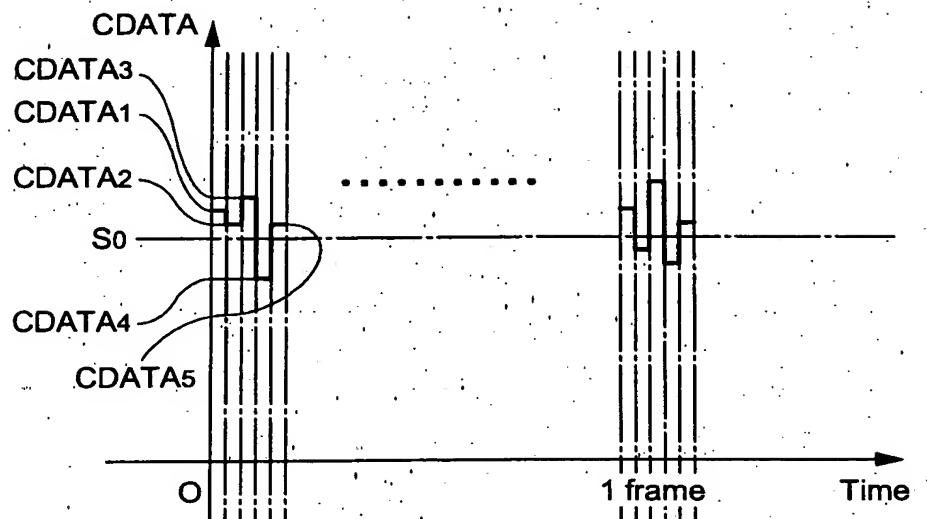
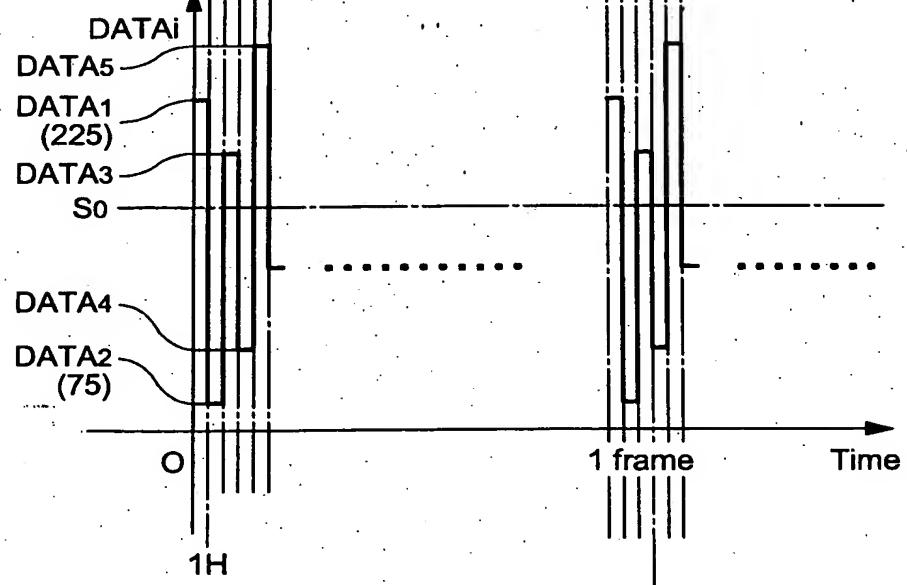


FIG. 17B



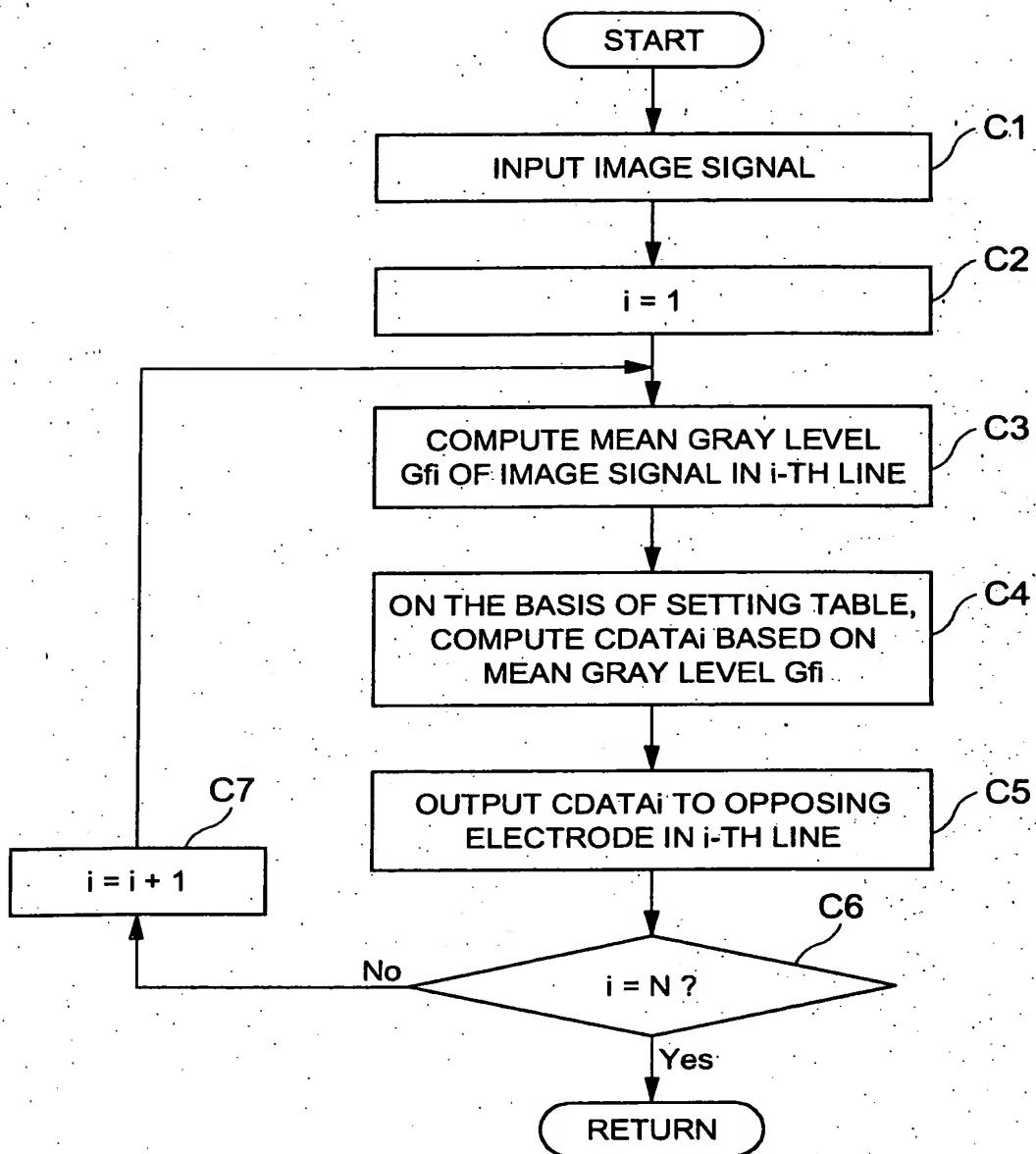


FIG. 18

FIG. 19

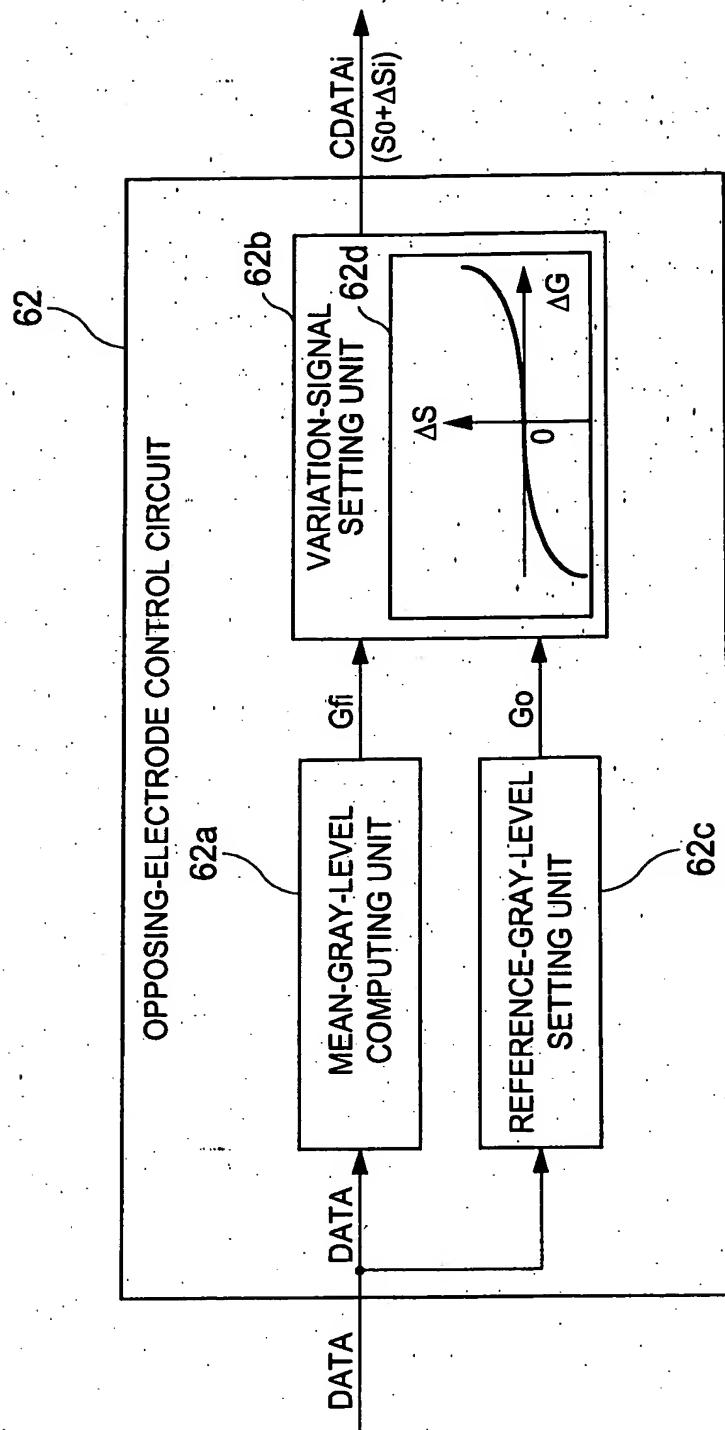


FIG. 20

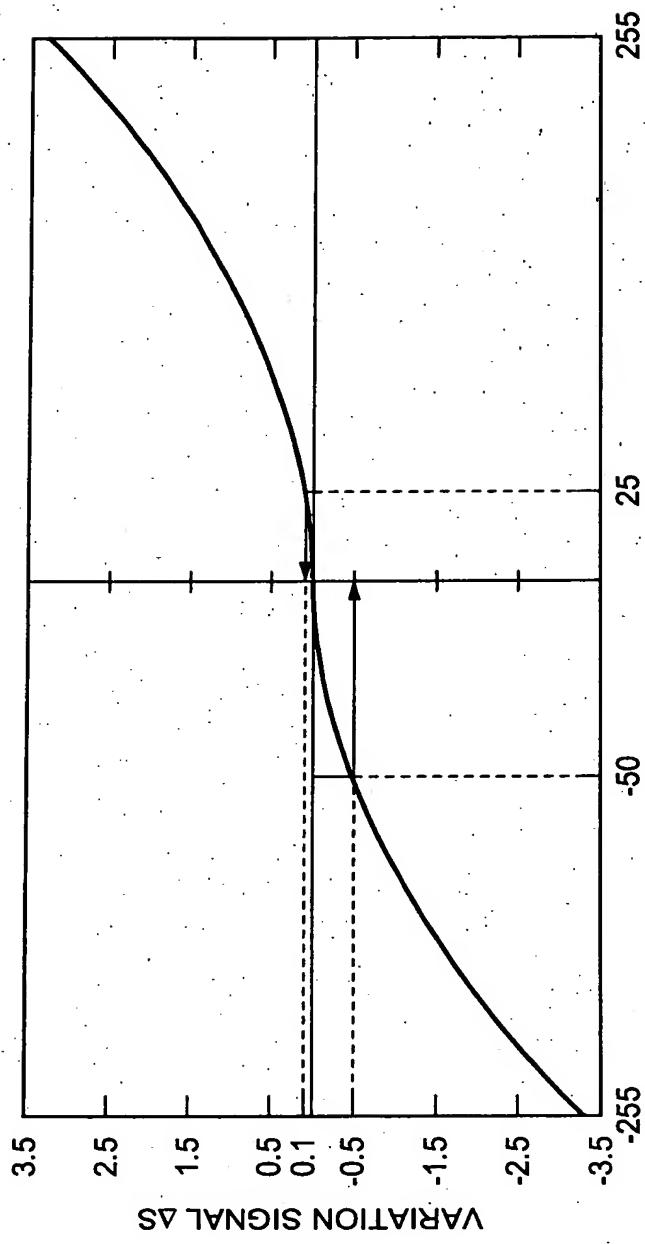


FIG. 21A

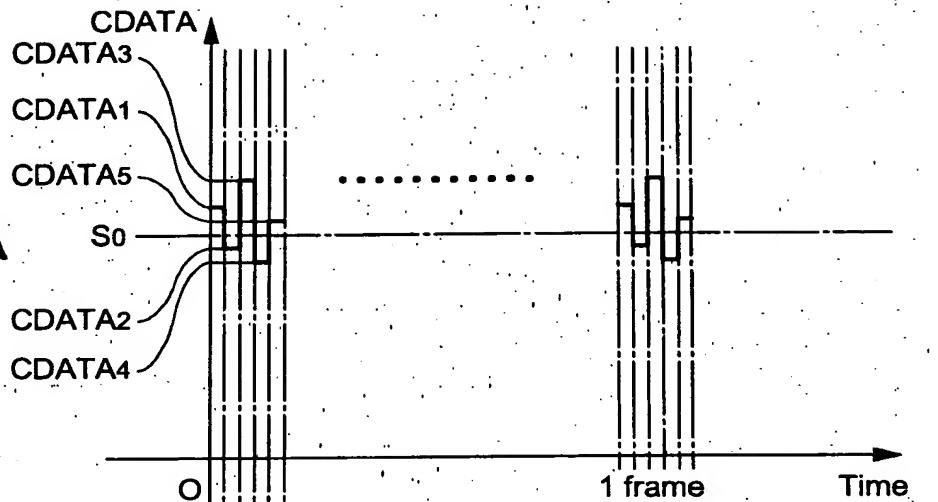
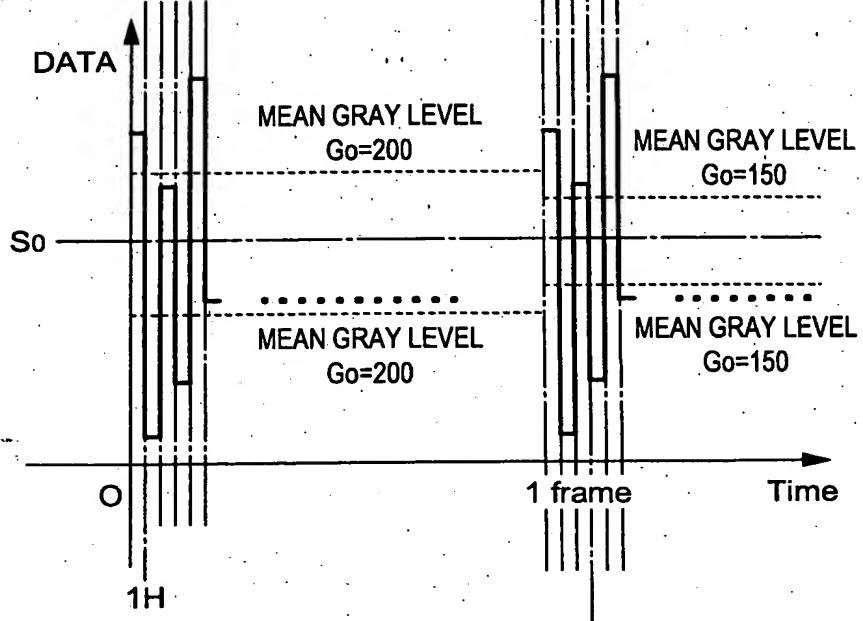


FIG. 21B



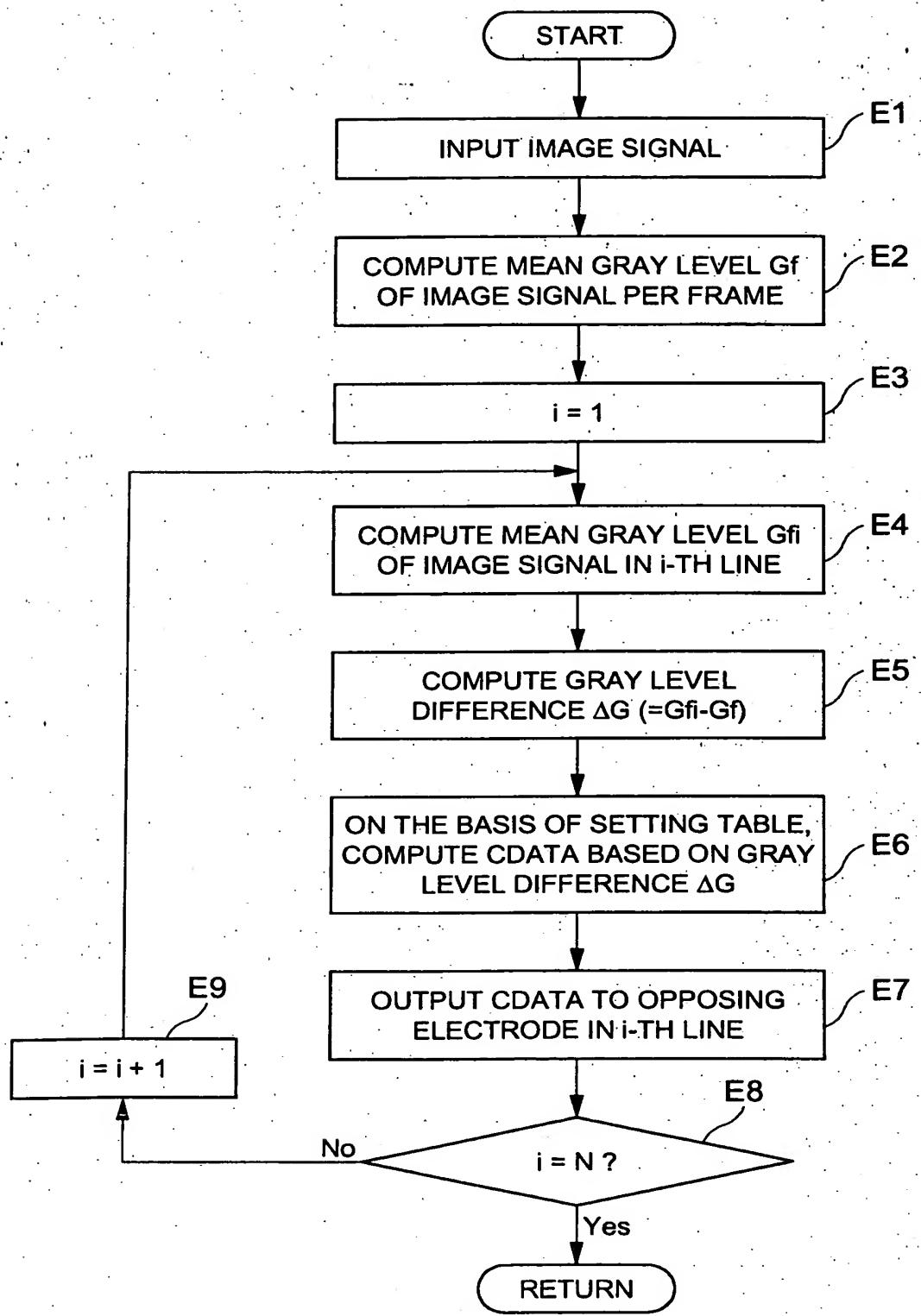
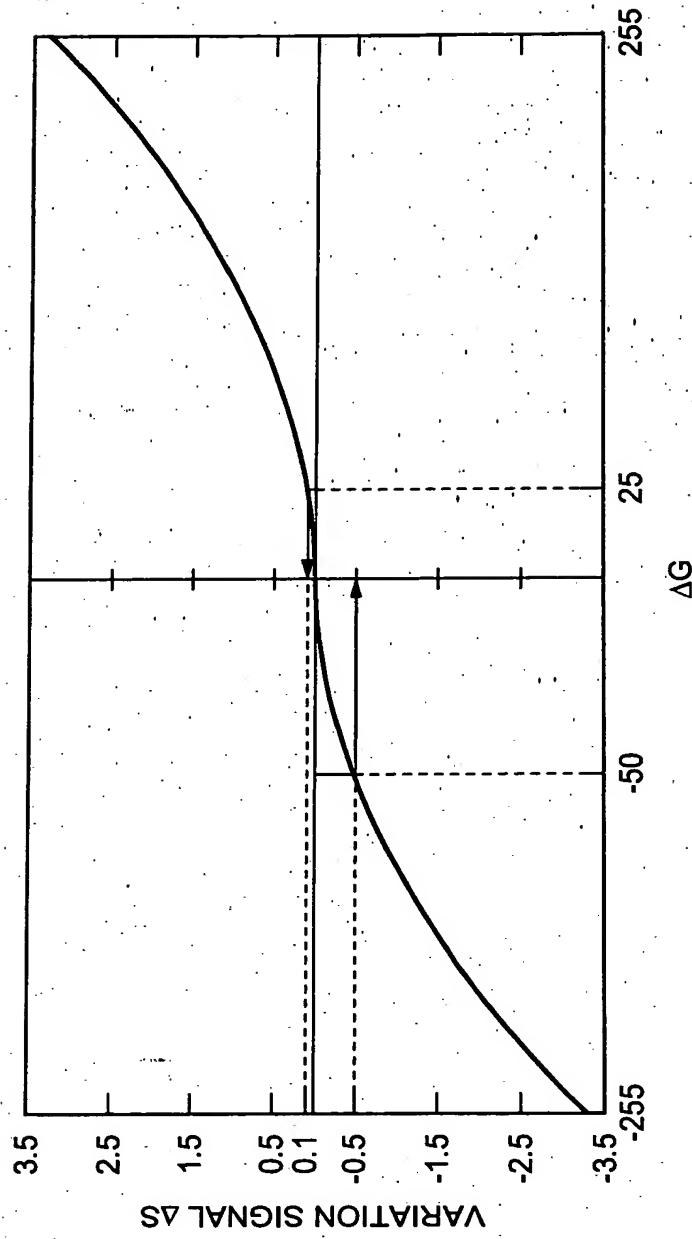


FIG. 22

FIG. 23



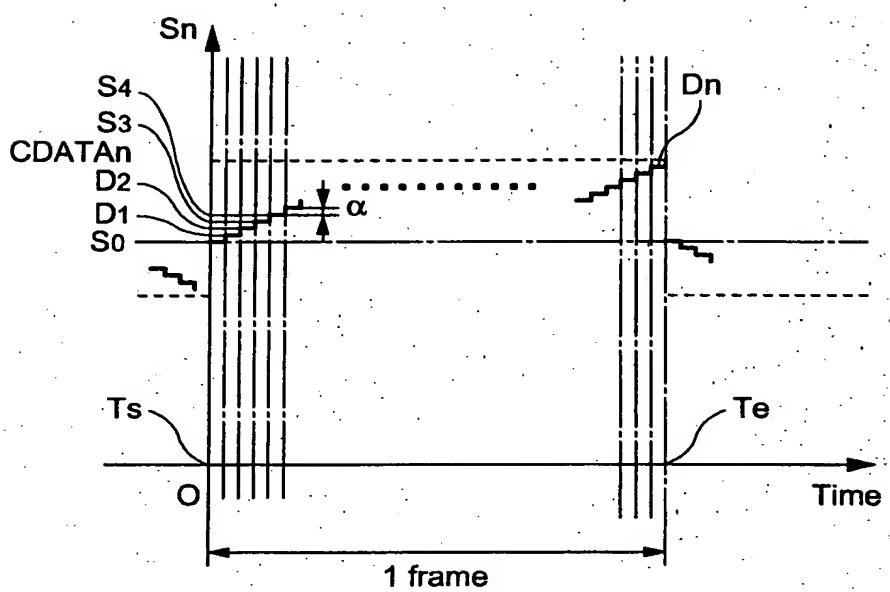


FIG. 24

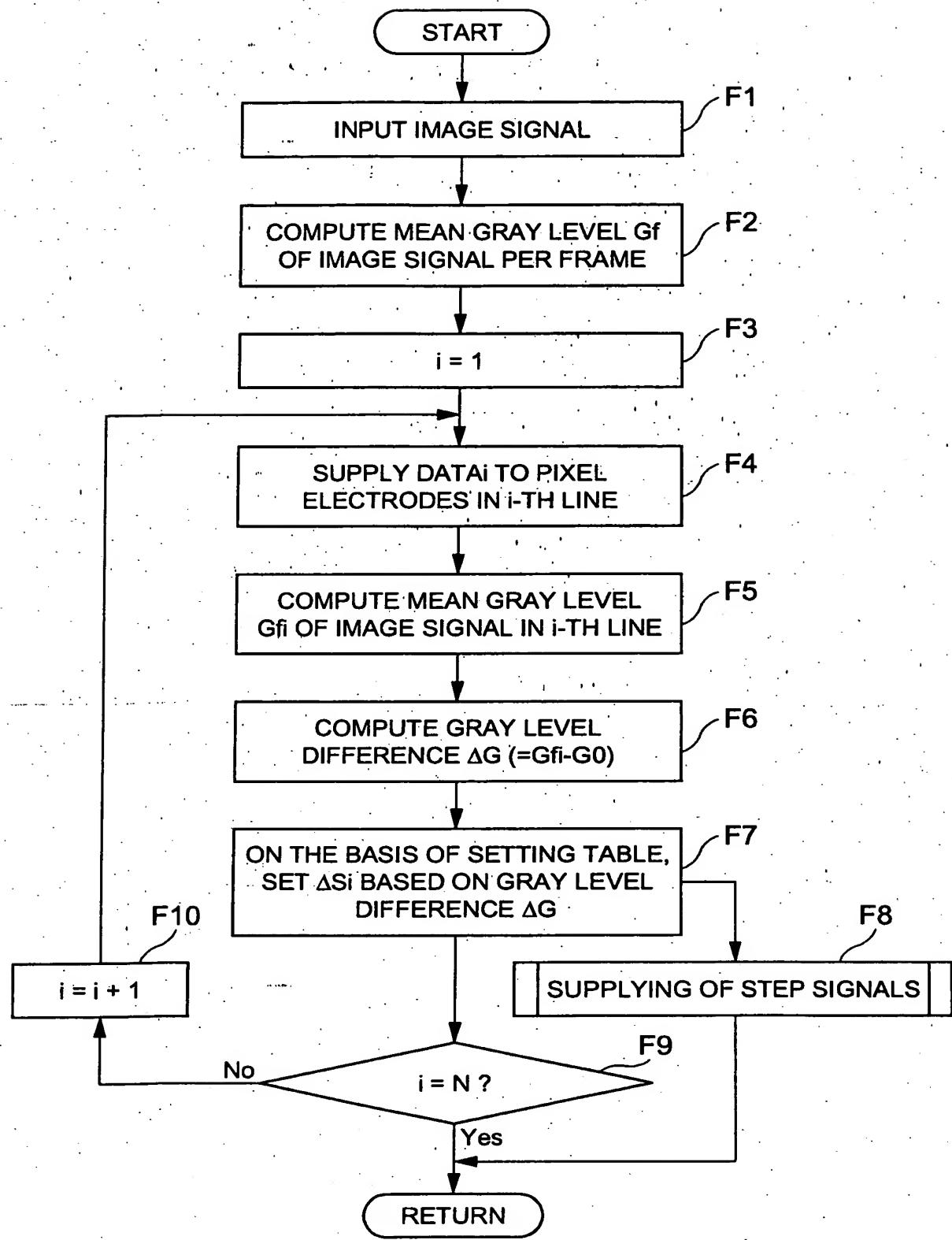


FIG. 25

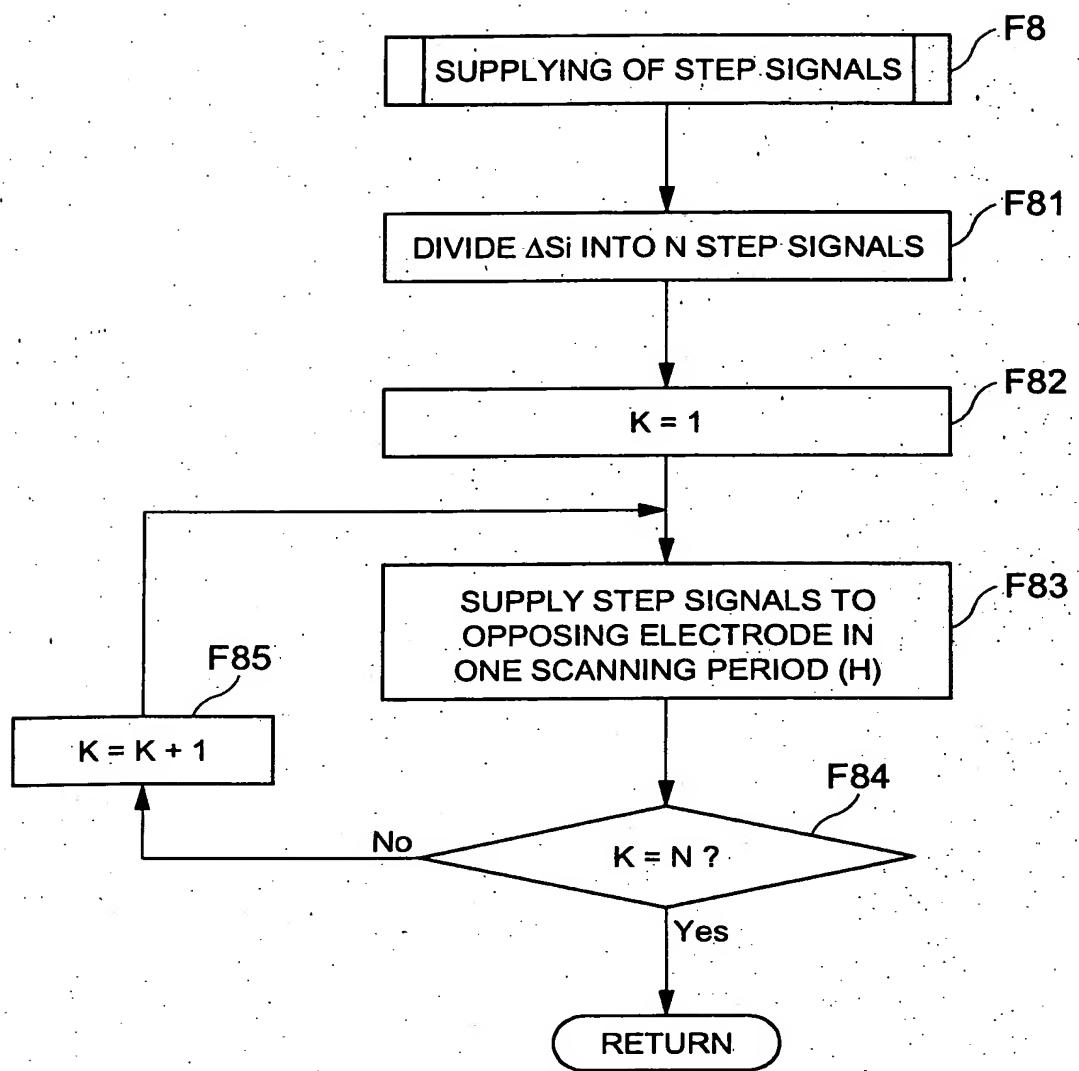
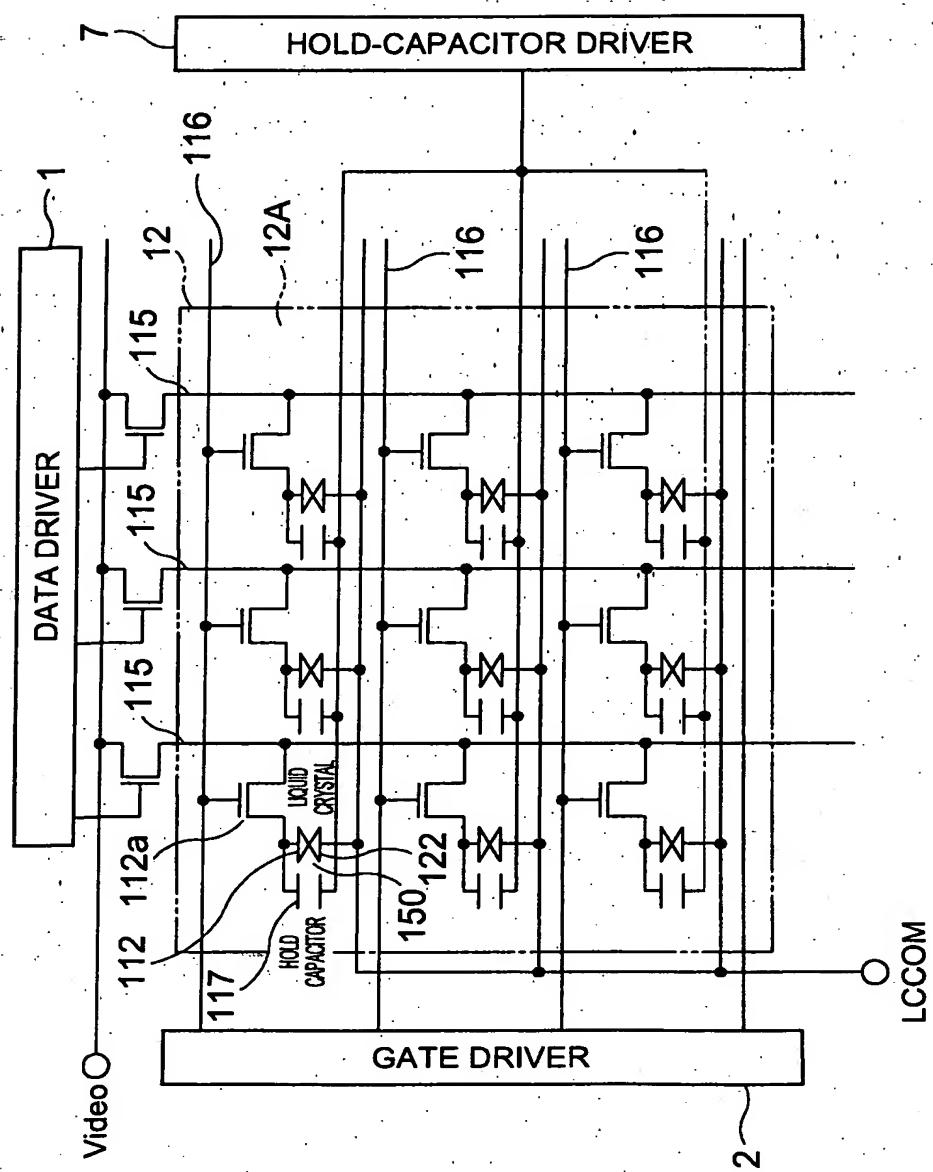


FIG. 26

FIG. 27



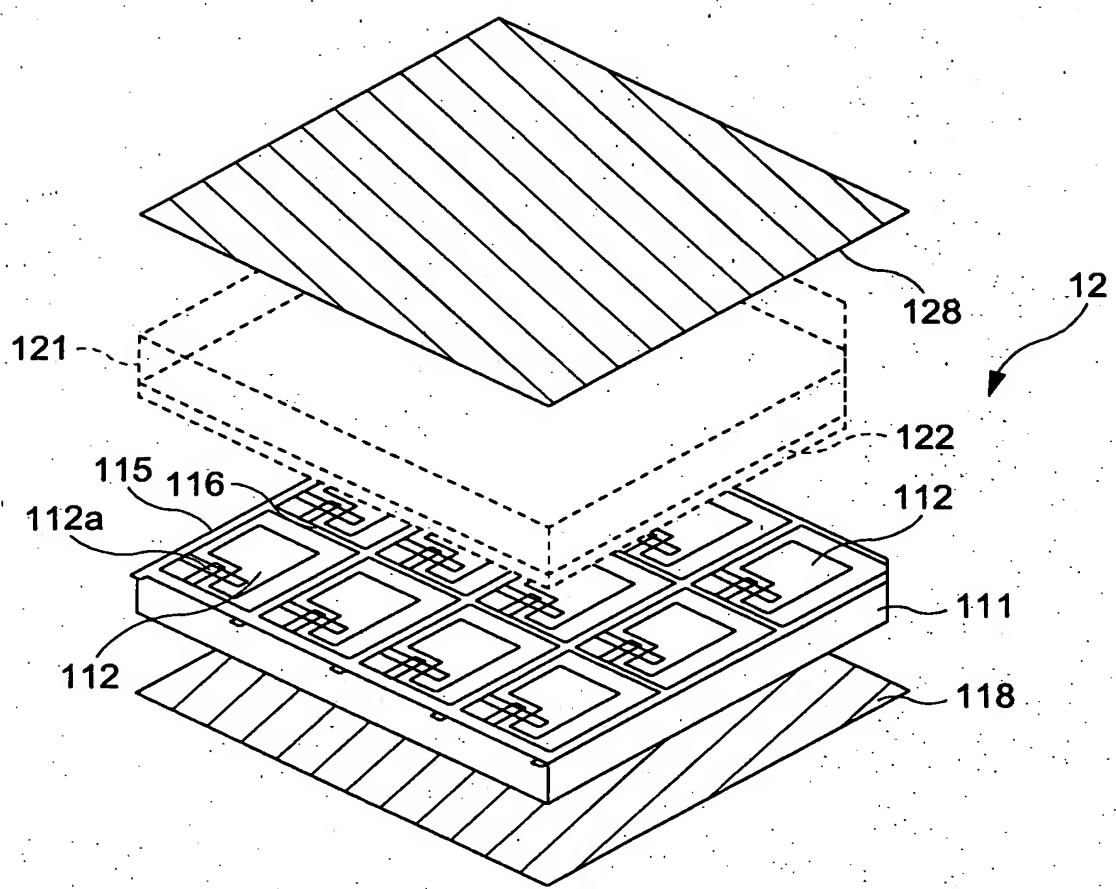


FIG. 28

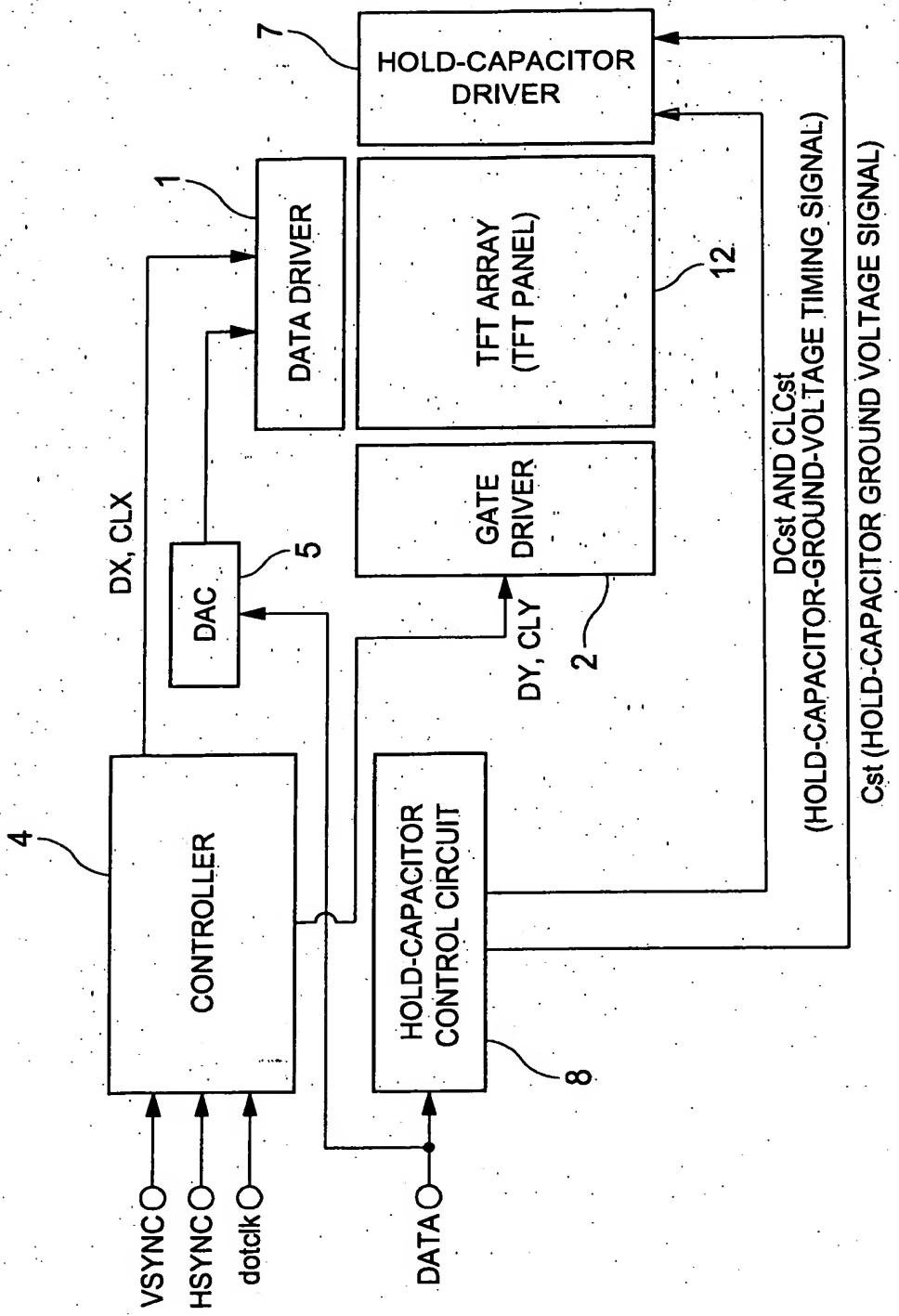
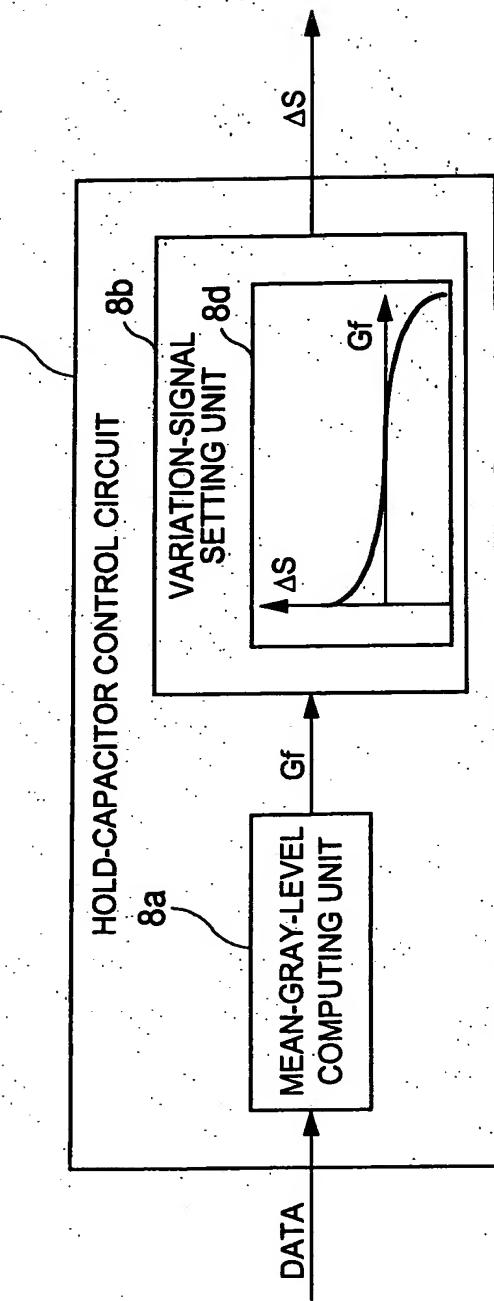


FIG. 29

FIG. 30



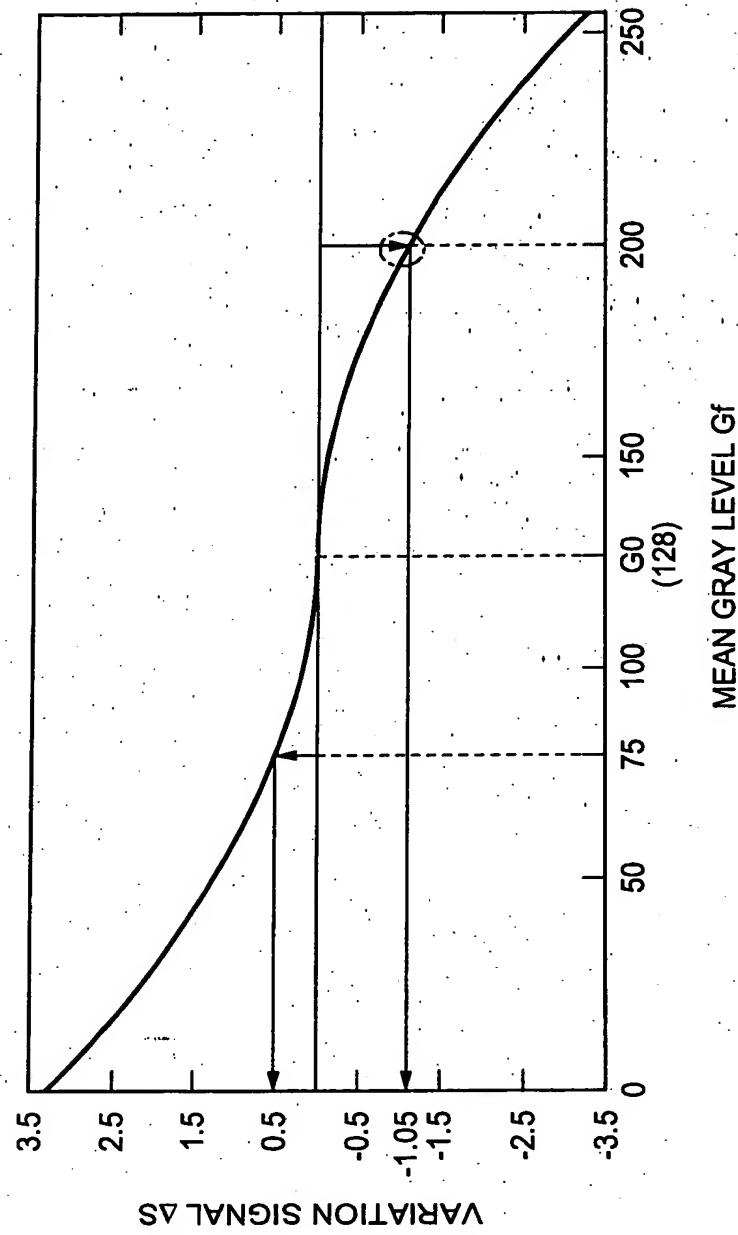


FIG. 31

FIG. 32A

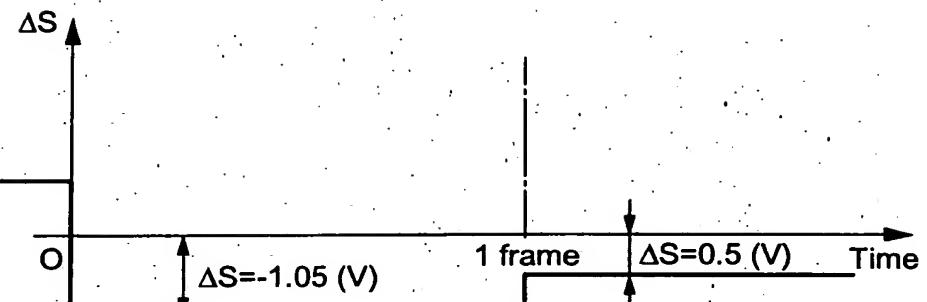
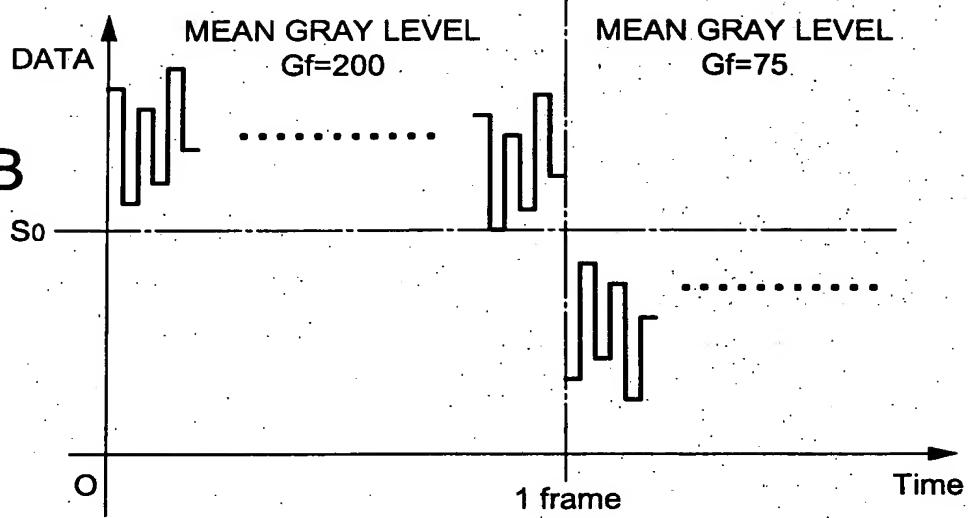


FIG. 32B



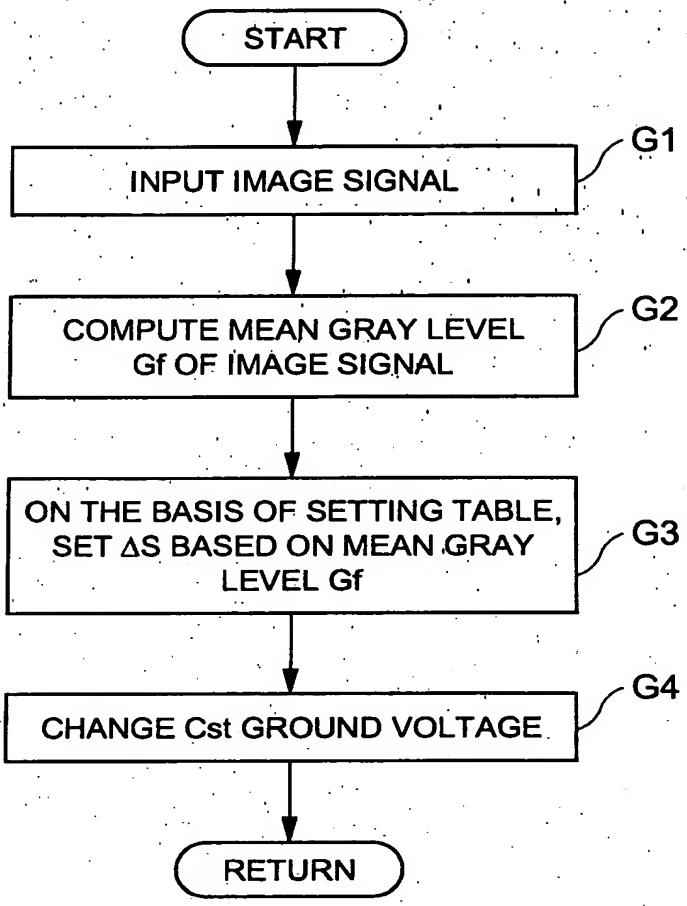


FIG. 33

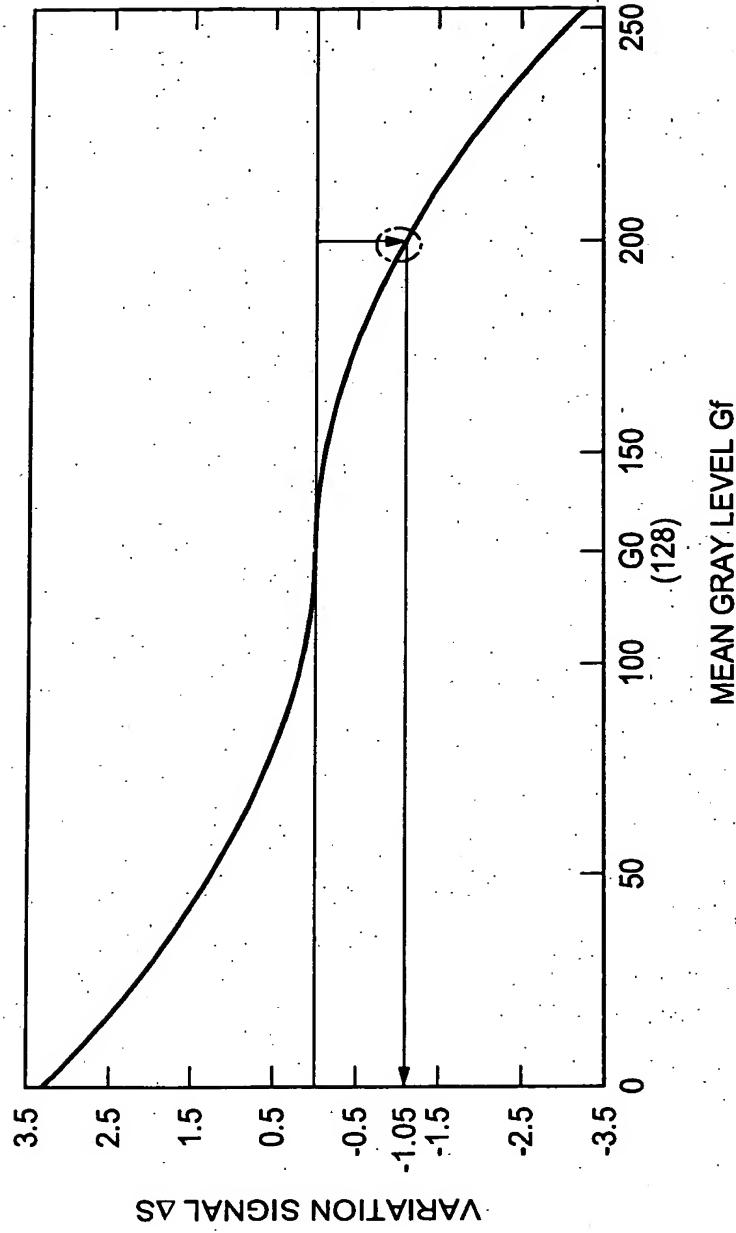


FIG. 34

FIG. 35A

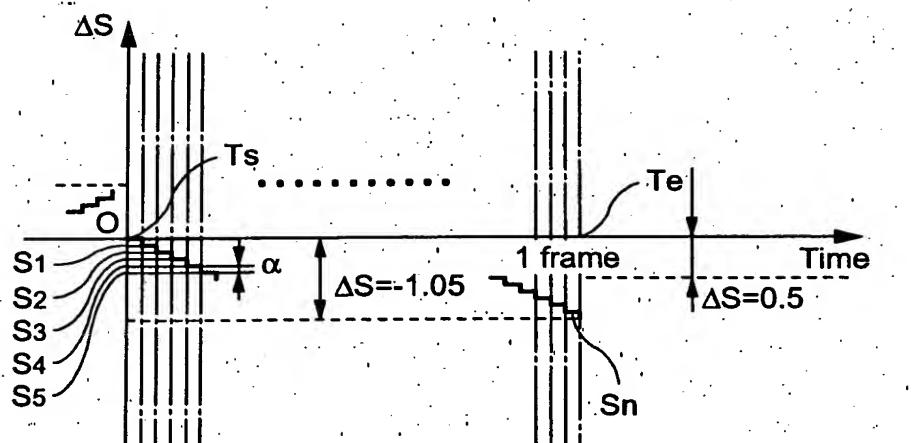
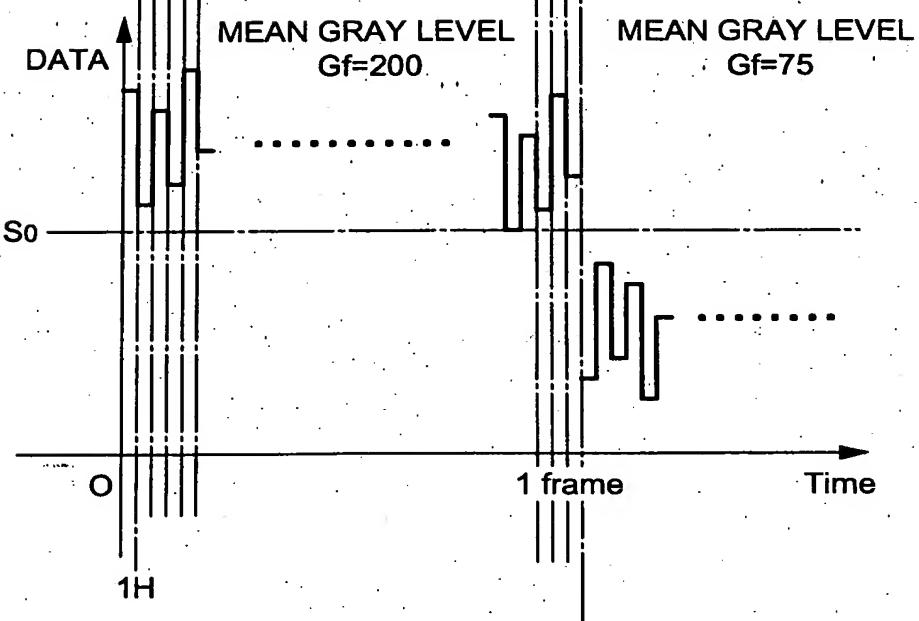


FIG. 35B



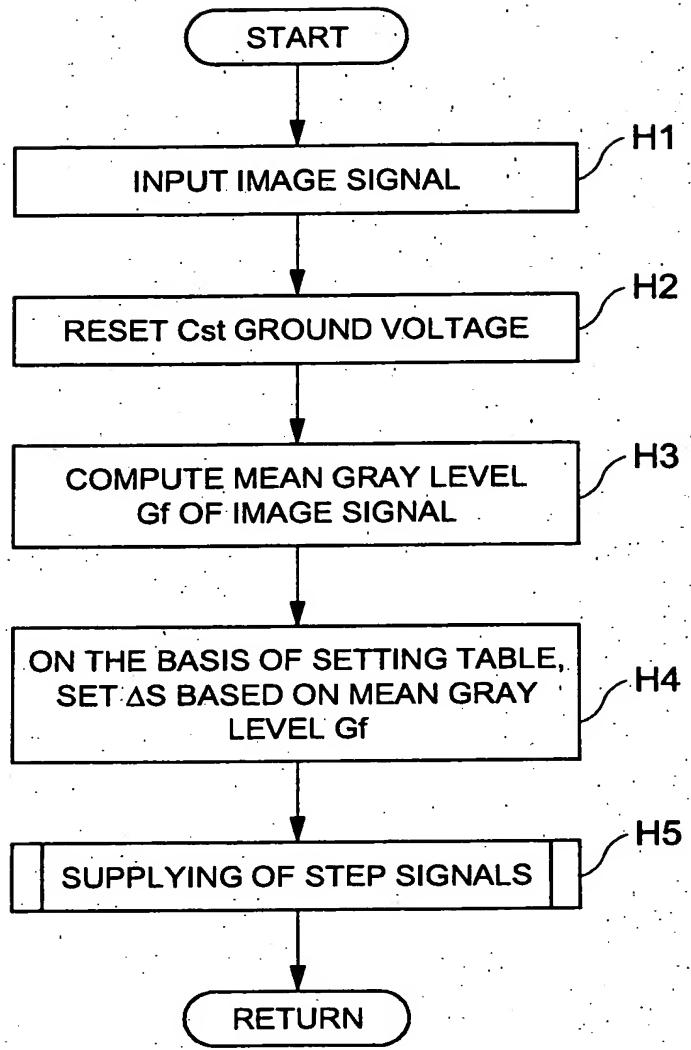


FIG. 36

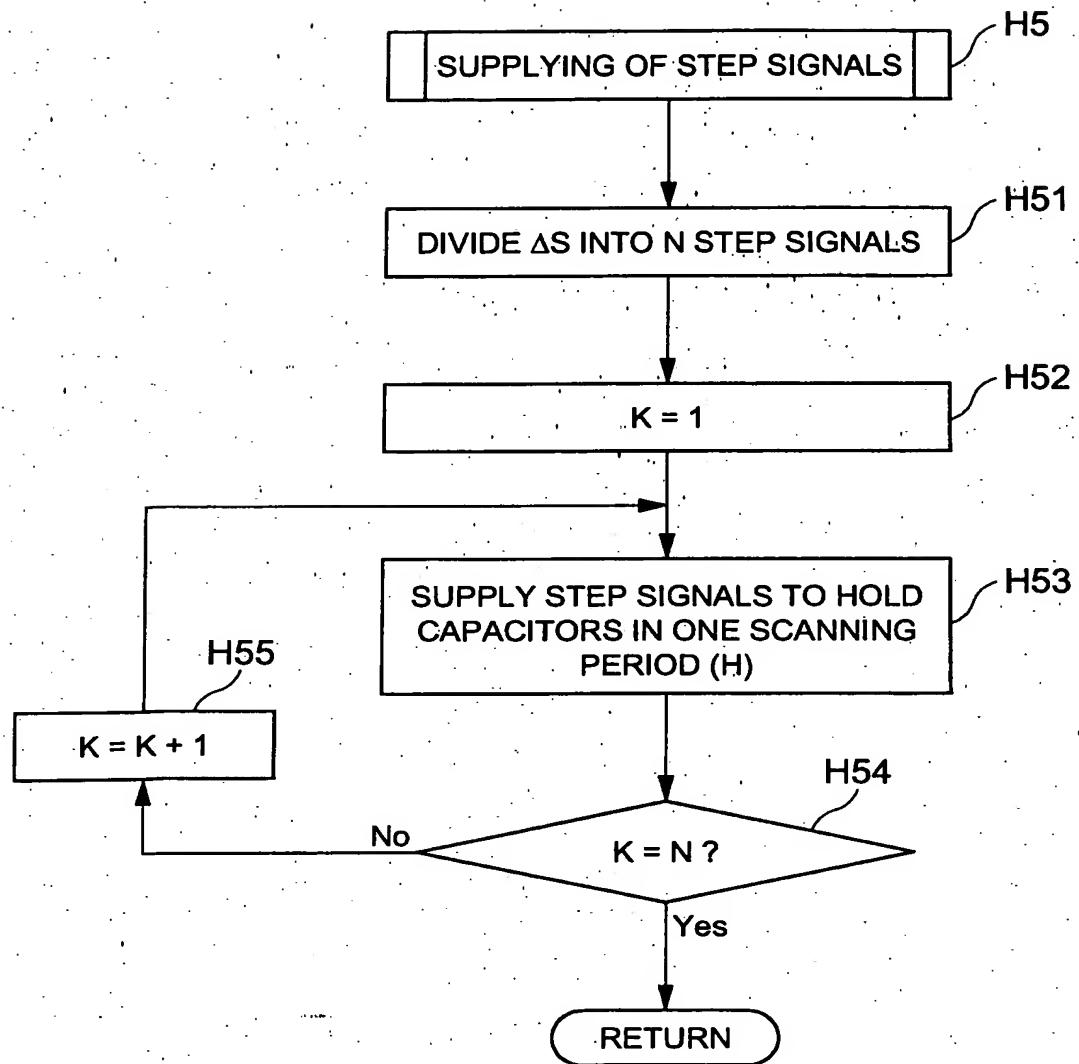


FIG. 37

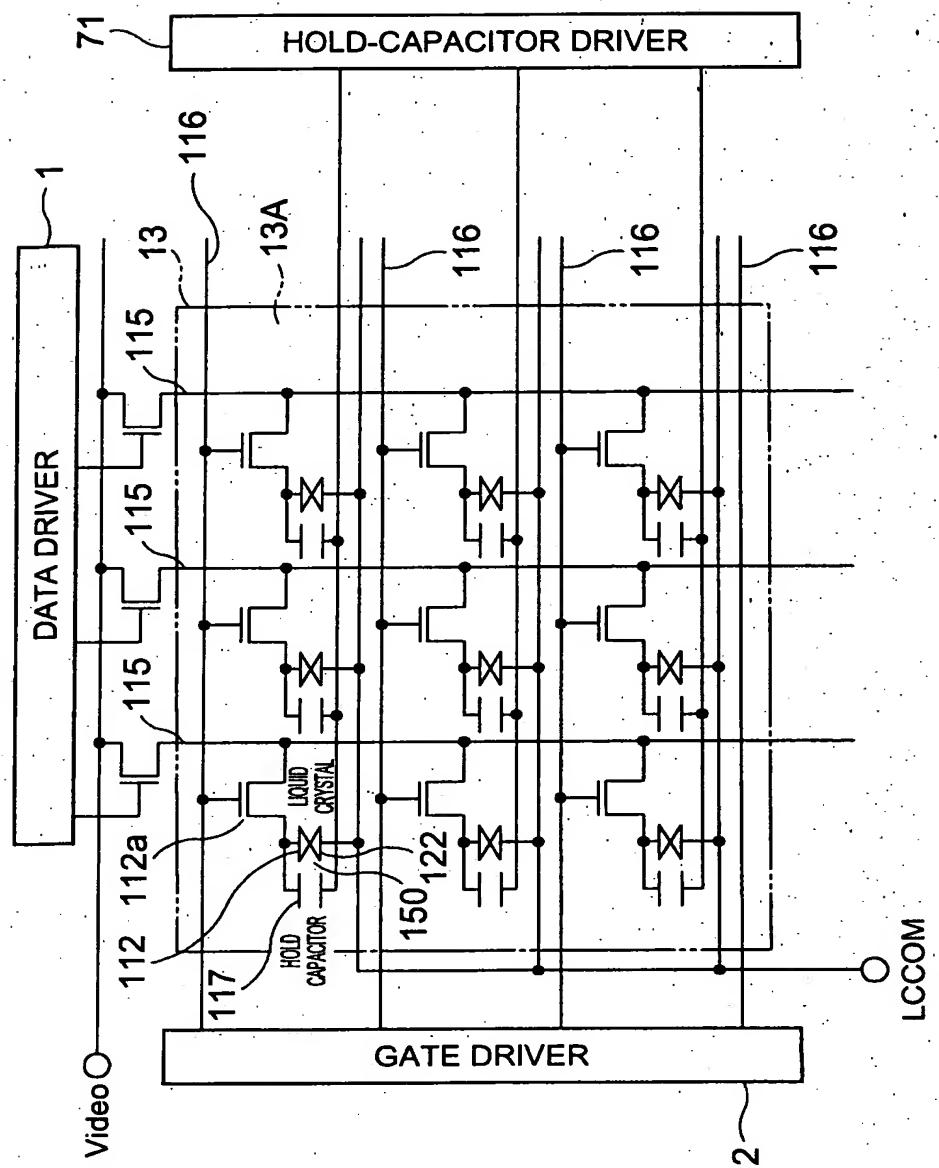


FIG. 38

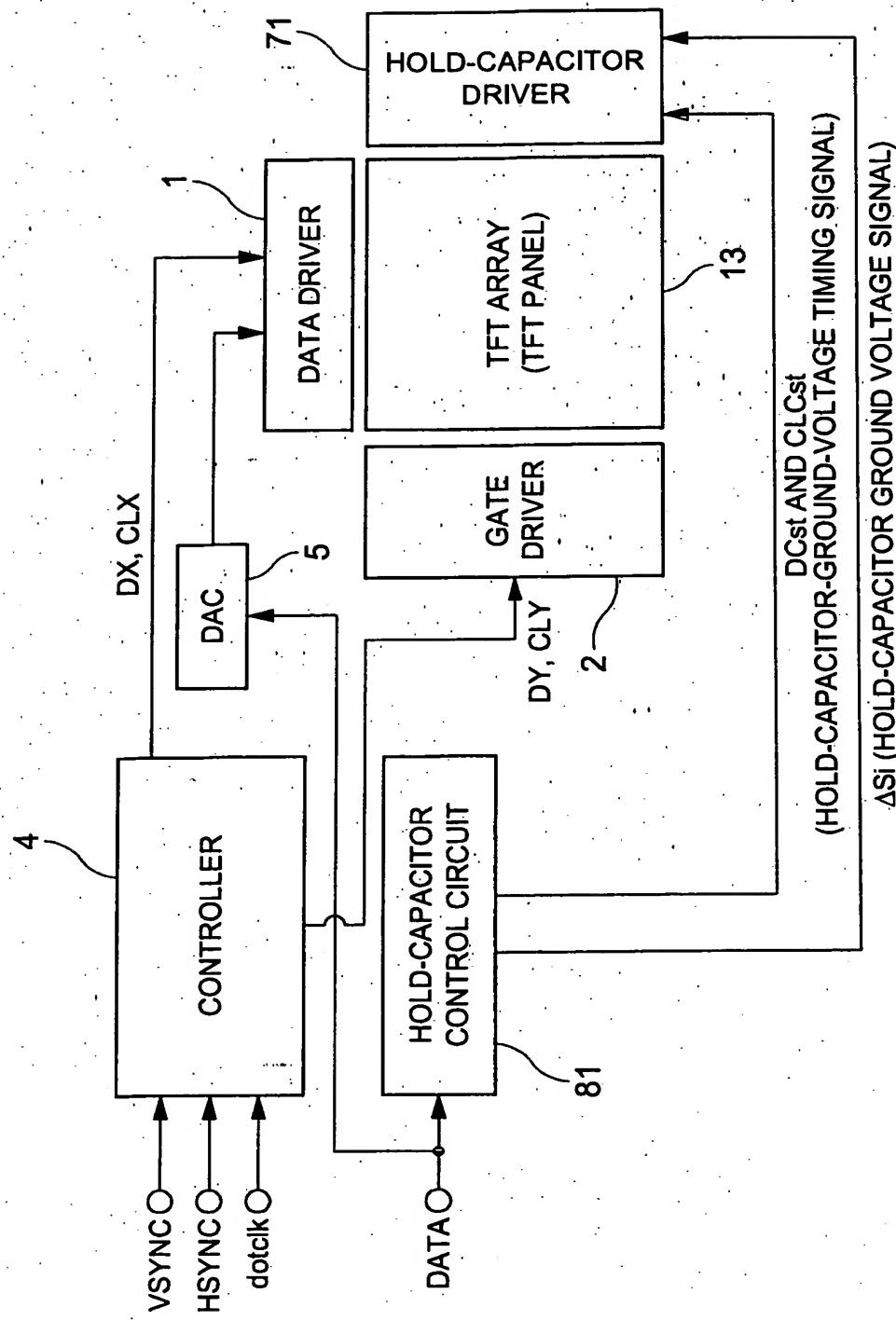
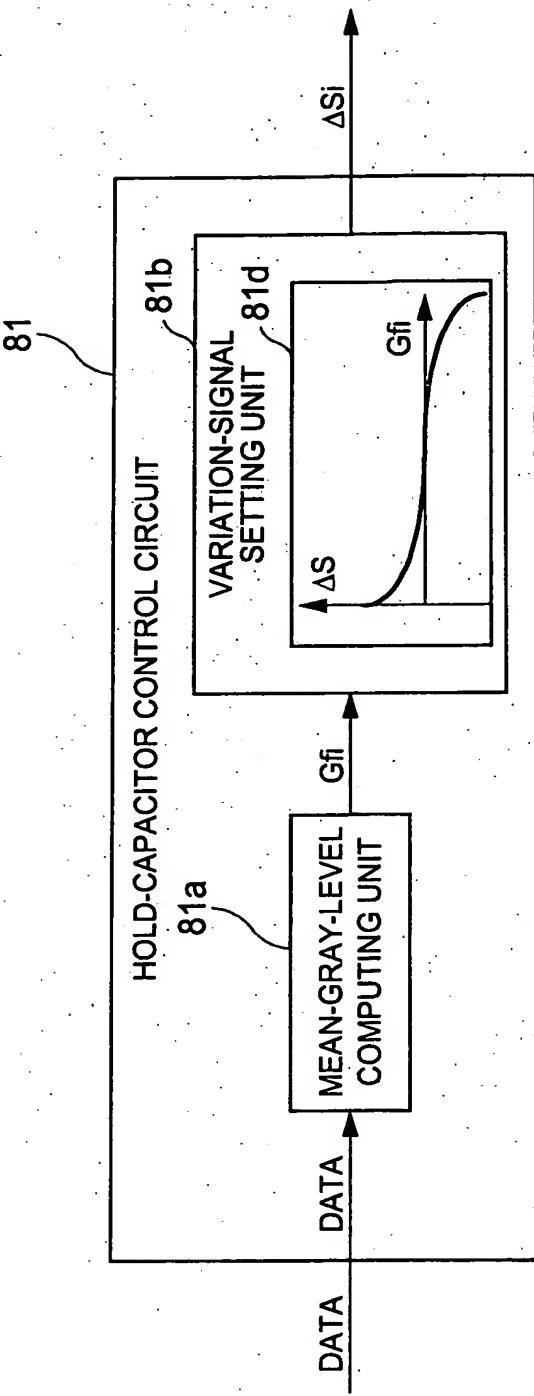


FIG. 39

FIG. 40



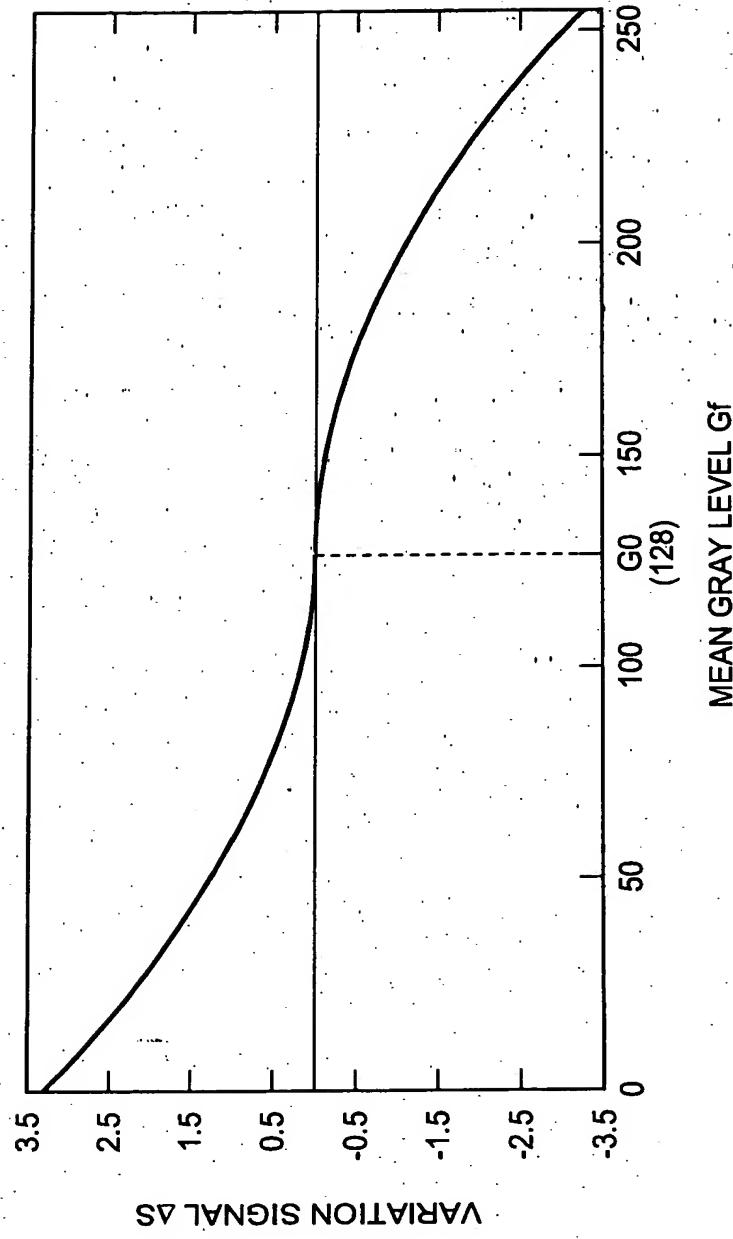


FIG. 41

FIG. 42A

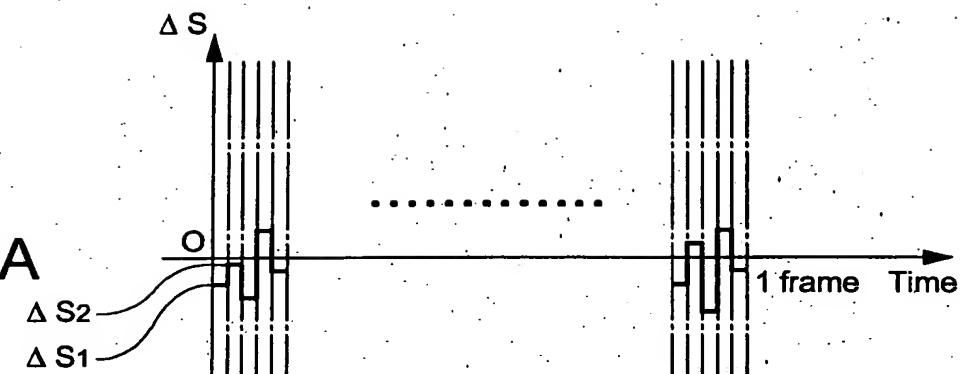
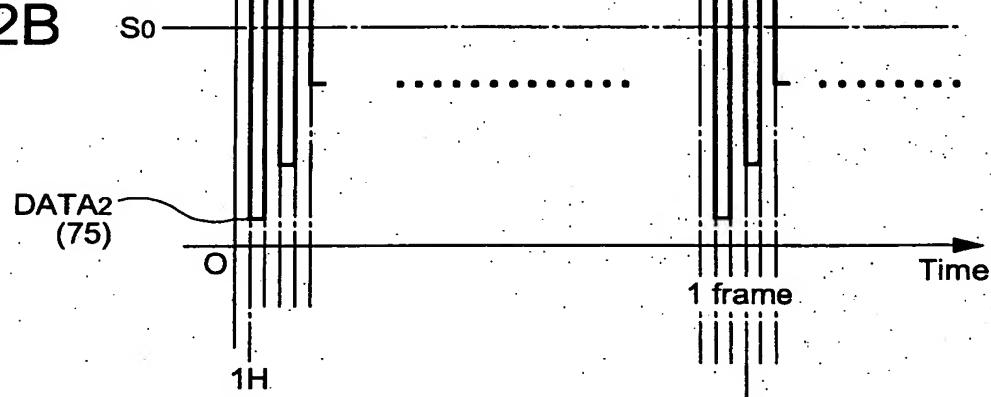


FIG. 42B



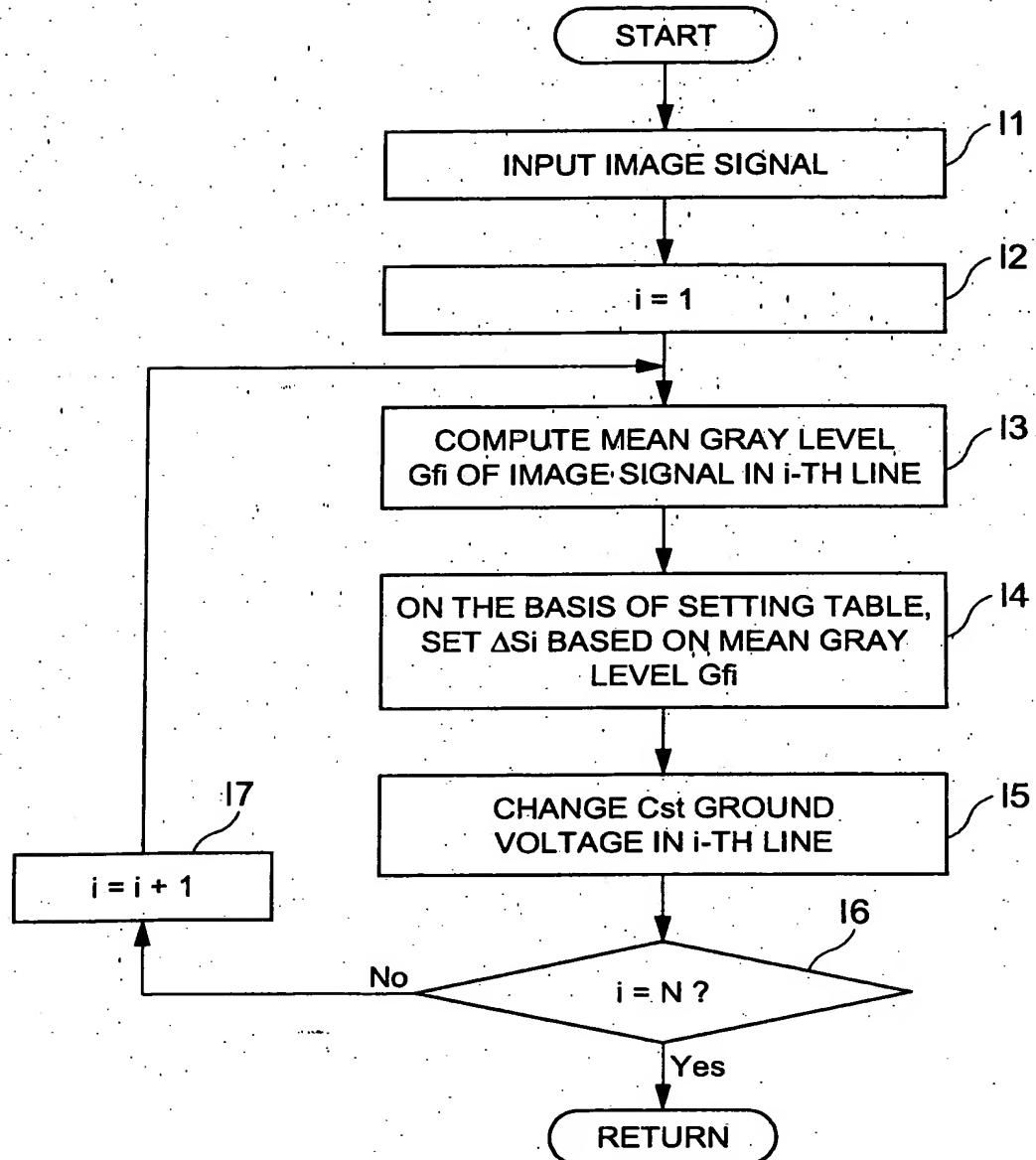


FIG. 43

FIG. 44

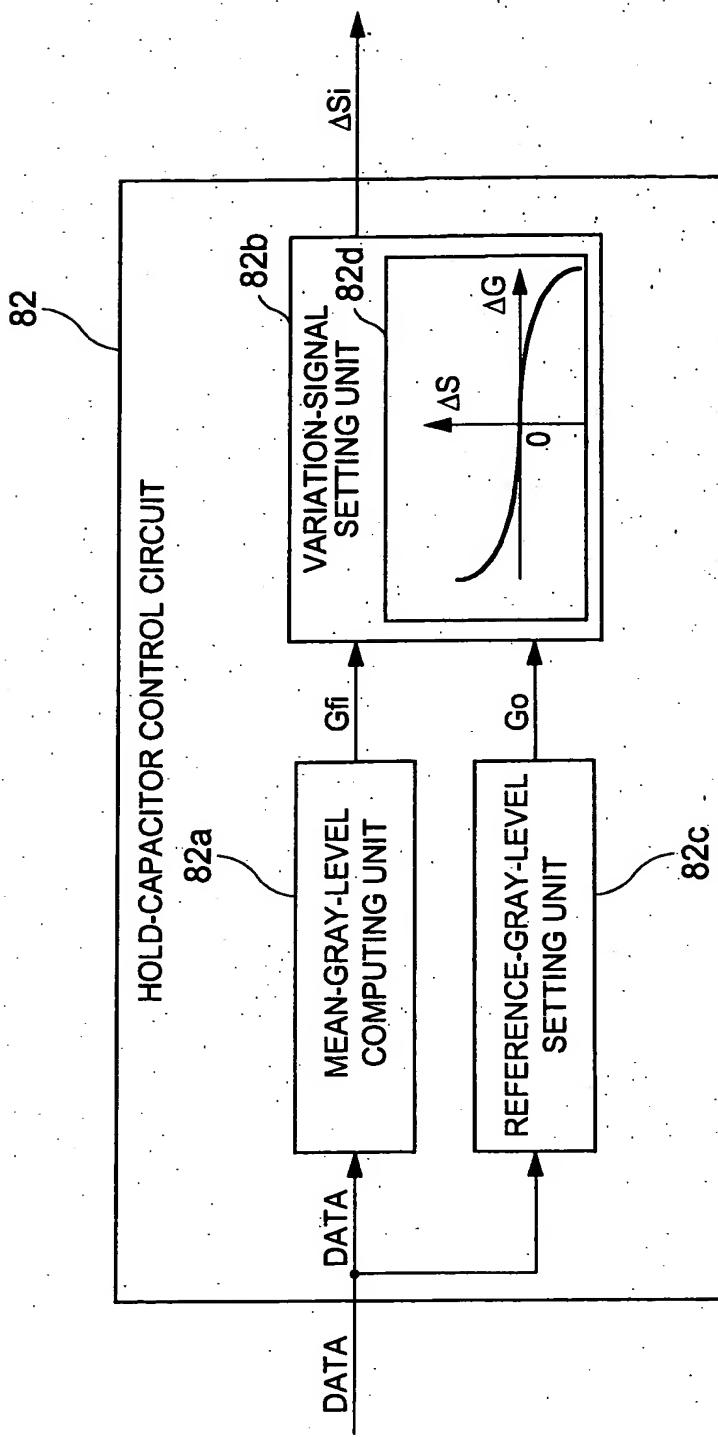


FIG. 45

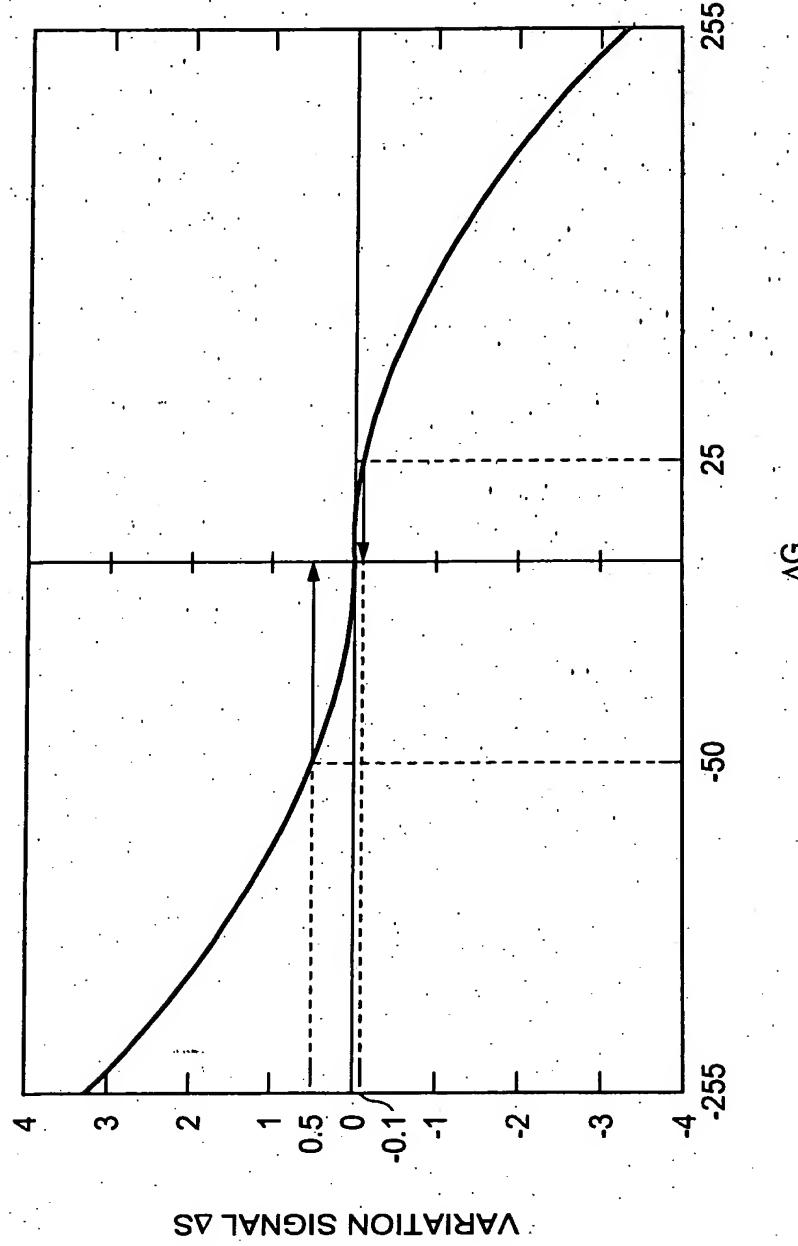


FIG. 46A

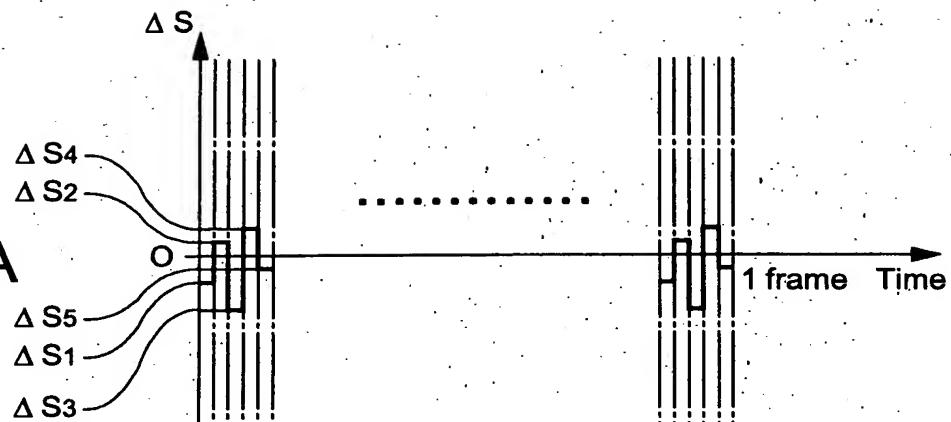
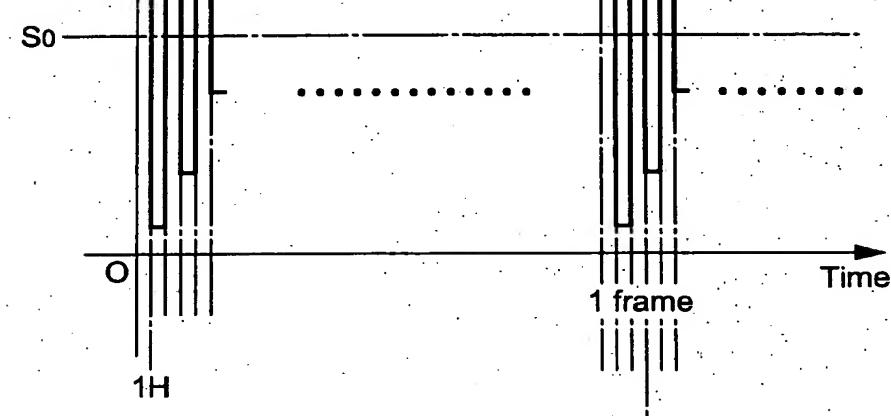


FIG. 46B



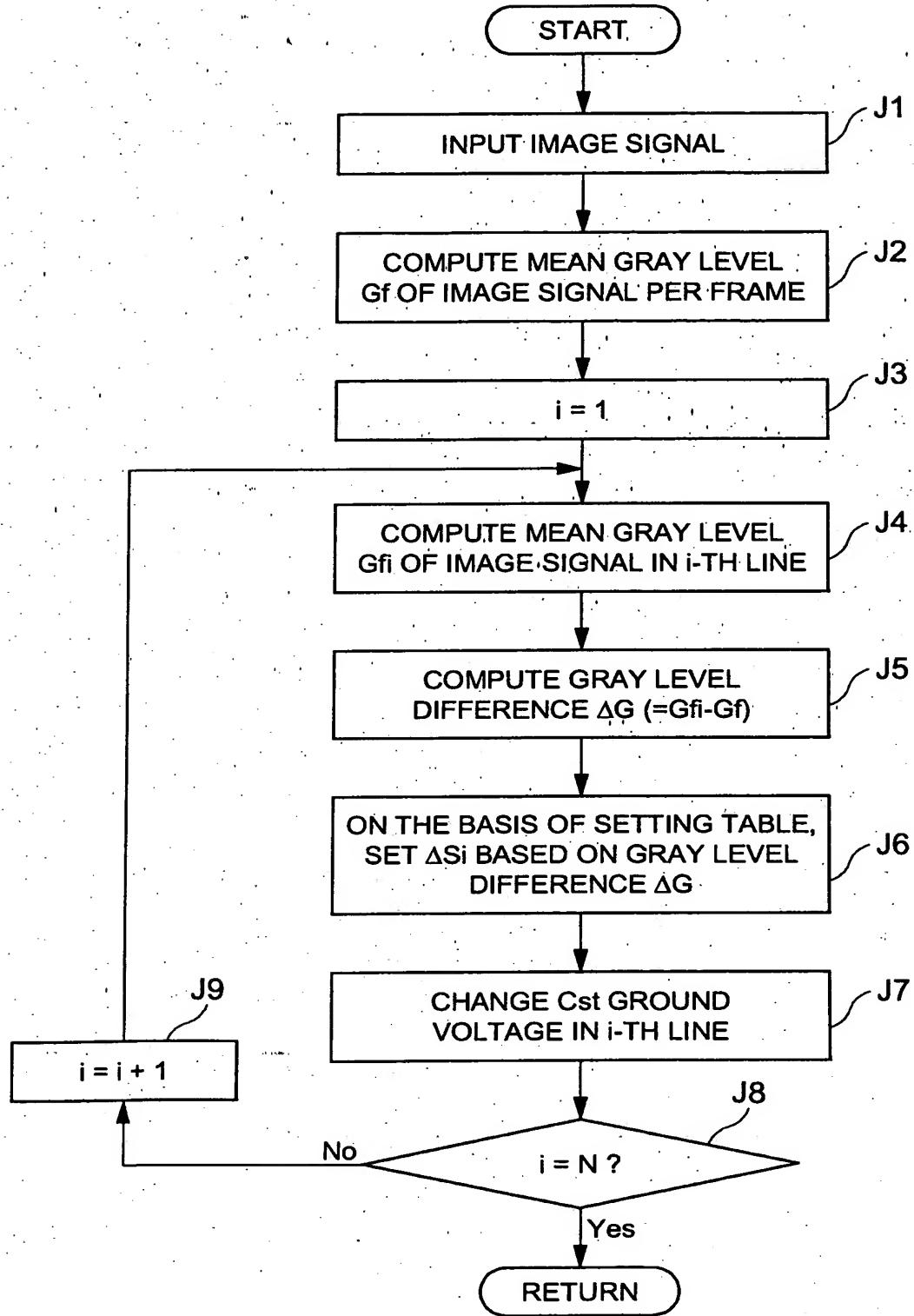
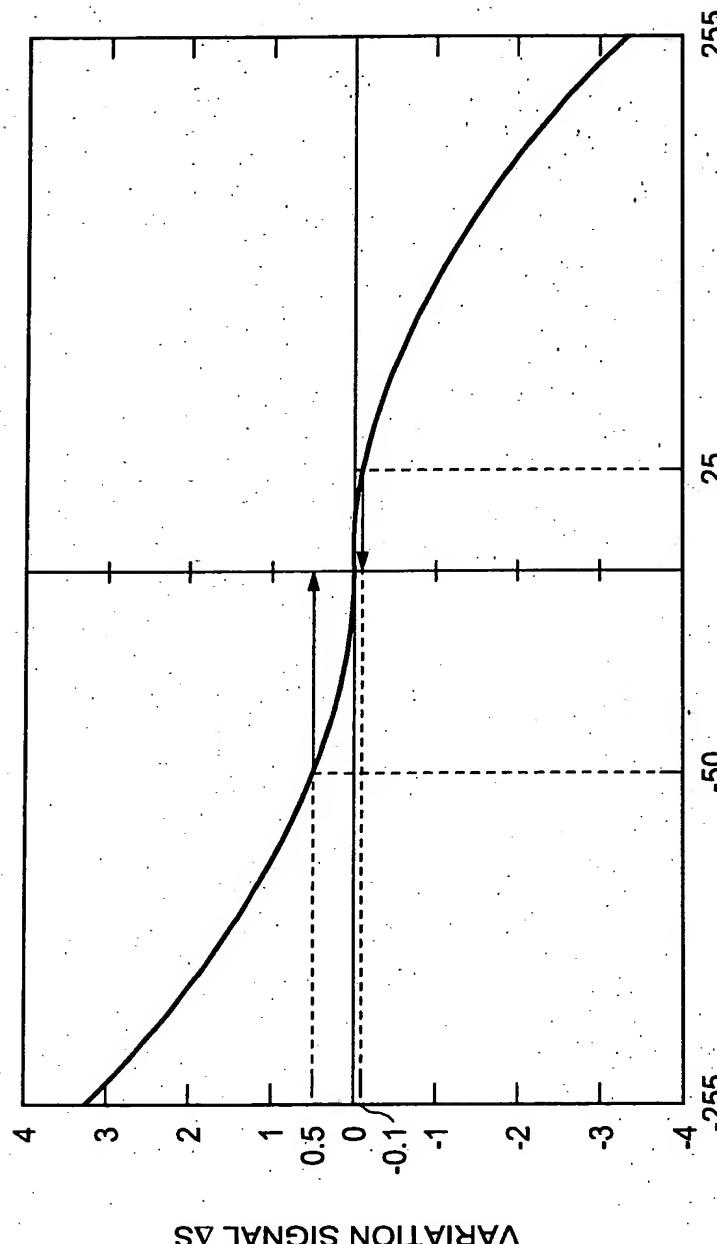


FIG. 47

FIG. 48



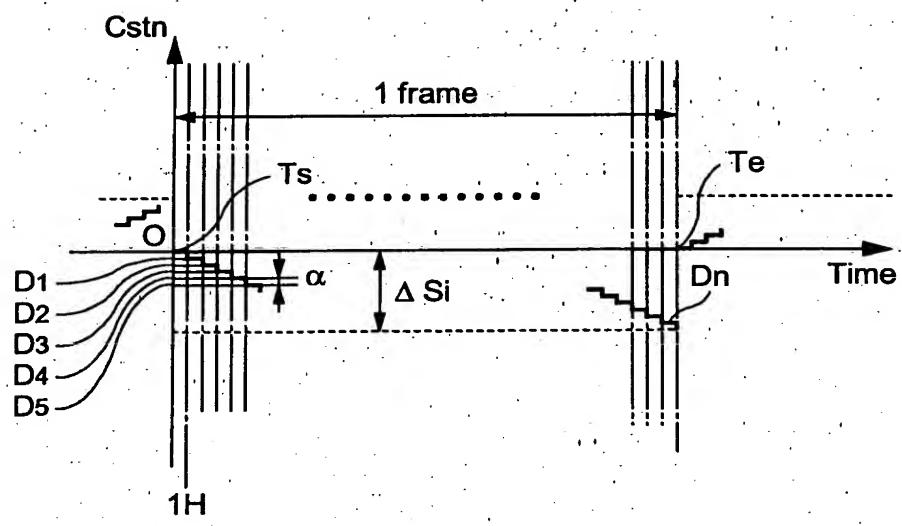


FIG. 49

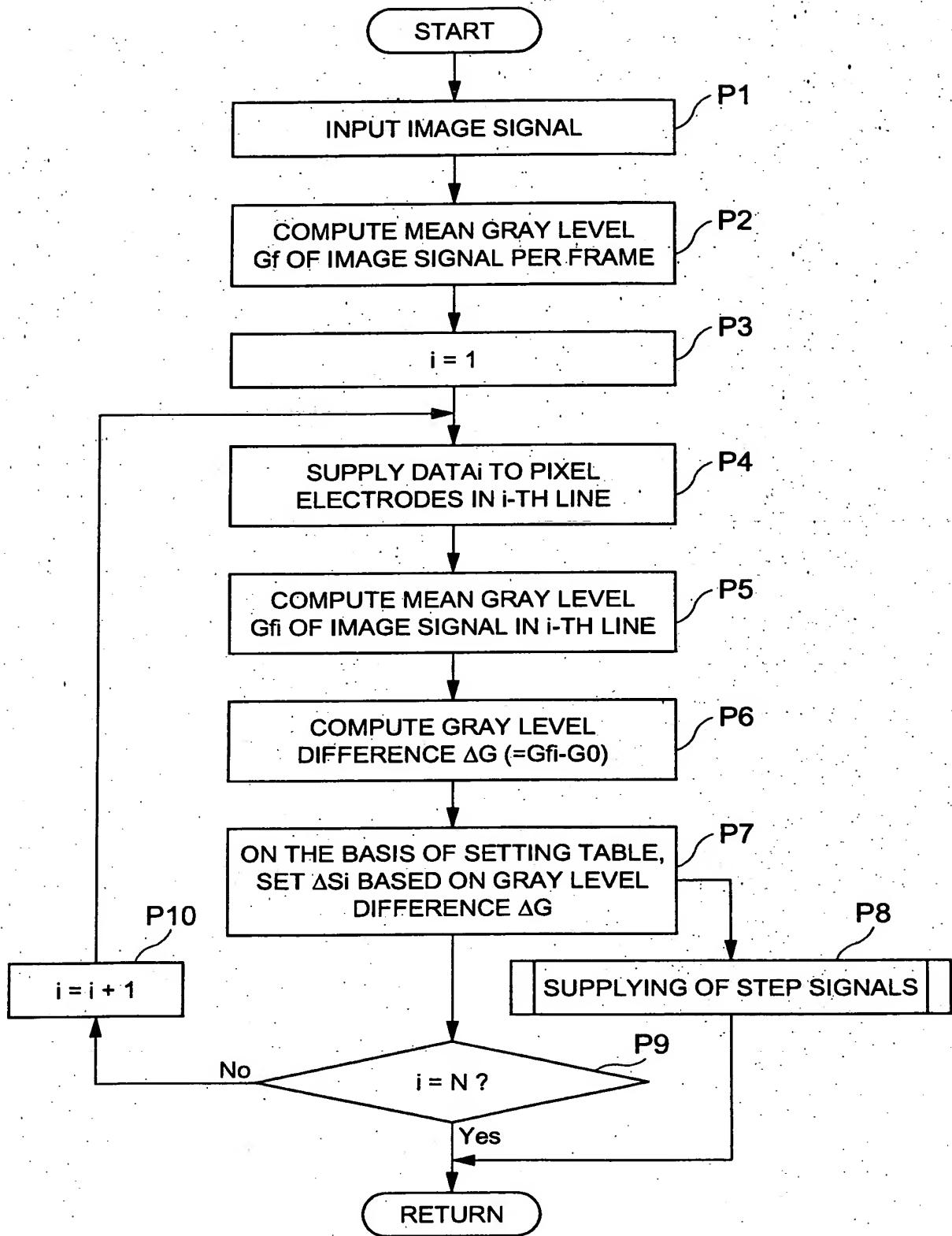


FIG. 50

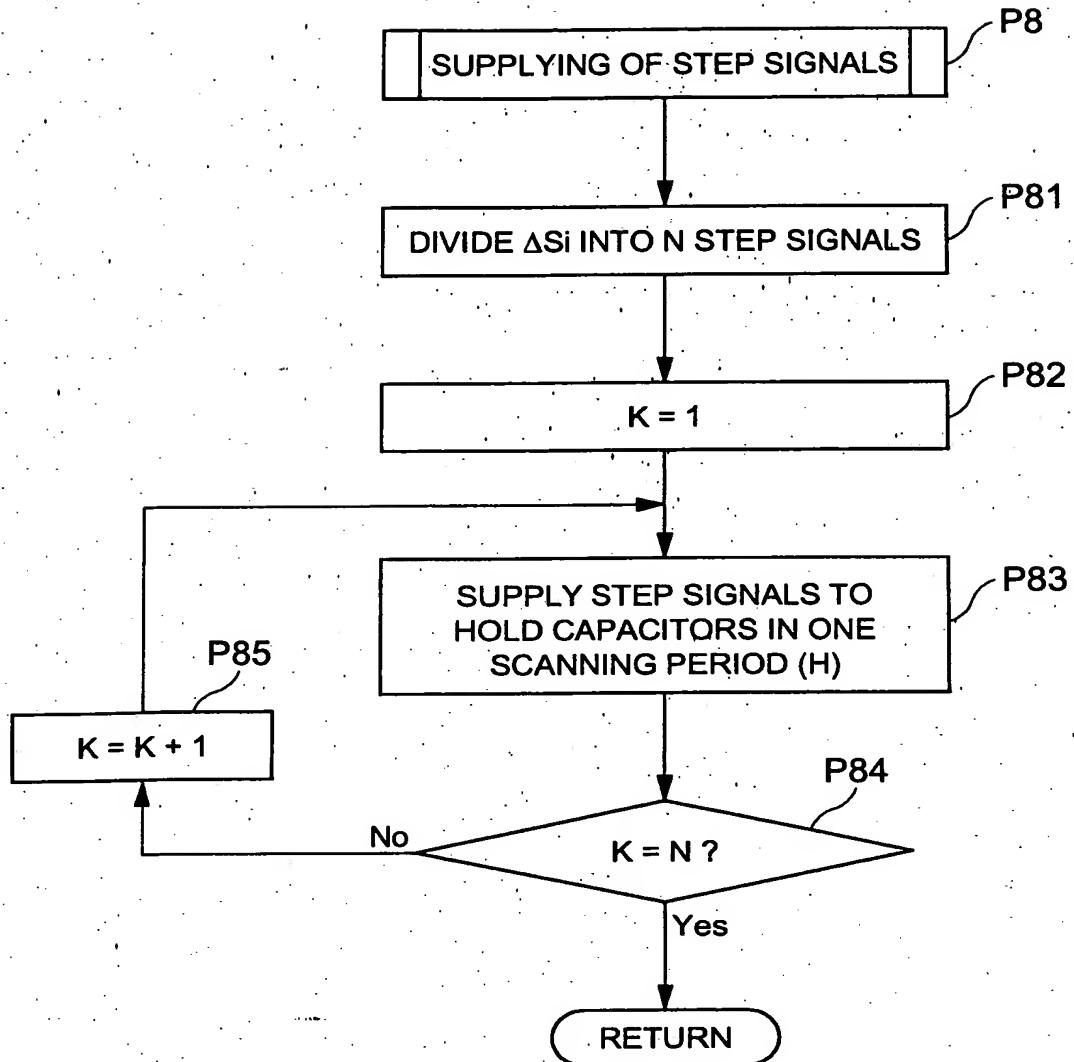
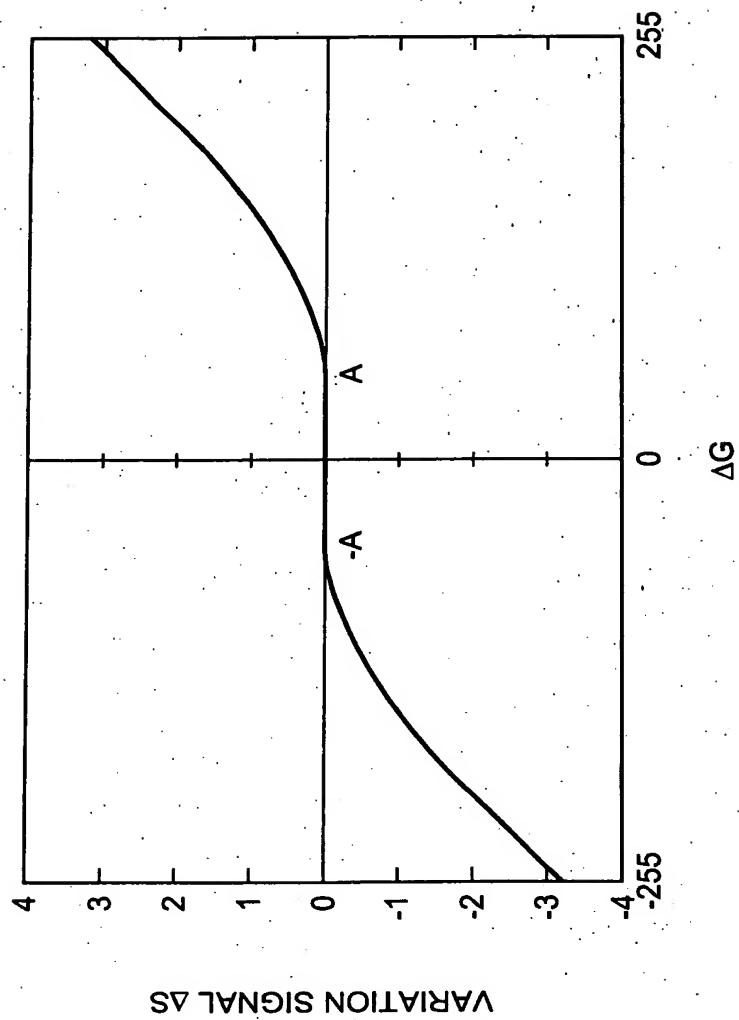


FIG. 51

FIG. 52



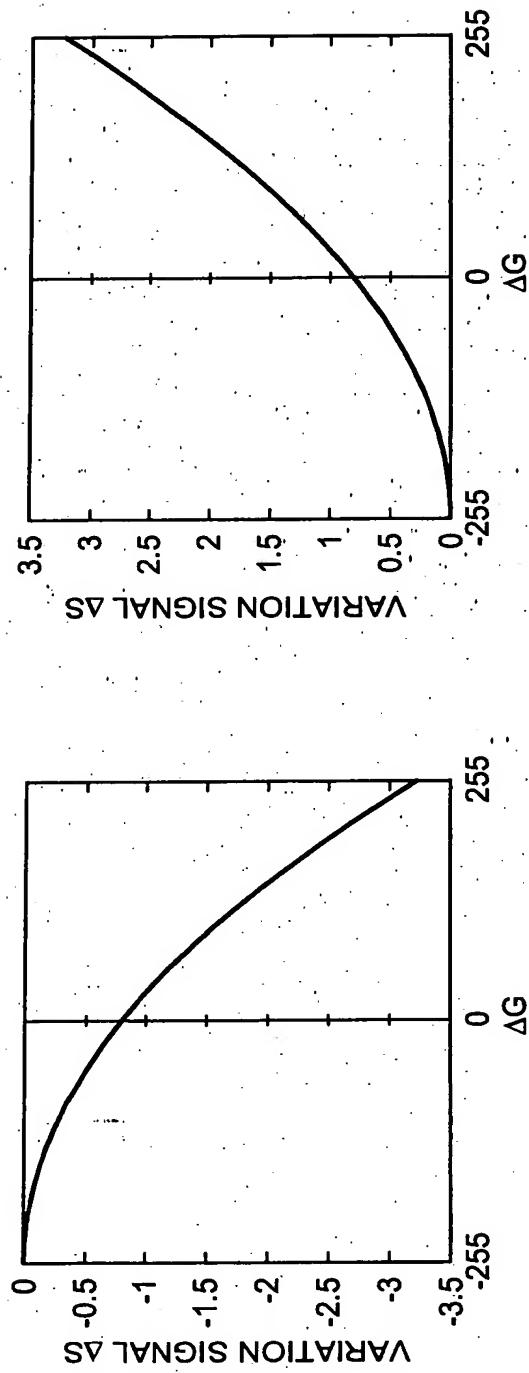


FIG. 53A
FIG. 53B

FIG. 54

